

CrossWorks

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FAE and Partner Kits

If you are an FAE or wish to support CrossWorks in your product, [please contact us!](#) We'll send you a complimentary **CrossWorks Kit!**

Quick Links

Downloads

[Version 4.6.0 for Windows \(x64\)](#)

[Version 4.6.0 for Windows \(x86\)](#)

[Version 4.6.0 for macOS](#)

[Version 4.6.0 for Linux \(x64\)](#)

Documentation

[CrossWorks for ARM V4 Frequently Asked Questions](#)

[Version 4.6.0 PDF Reference Manual](#)

[Version 4.6.0 HTML Reference Manual](#)

[Version 4.6.0 Release Notes](#)

[Support Packages](#)

[Supported ARM Devices](#)

[GCC and BINUTILS Source Code](#)

[LLVM/Clang Source Code](#)

[License Information](#)

[Frequently Asked Questions](#)

Supported Cores

CrossWorks for ARM supports the following cores:

- Cortex-M, Cortex-A, Cortex-R
- ARM11, ARM9, ARM7
- Feroceon, XScale

System Requirements

Operating Systems

- Windows 10, Windows 8, Windows 7, Windows Vista*
- macOS 10.15 Catalina, macOS 10.14 Mojave, 10.13 High Sierra, 10.12 Sierra, 10.11 El Capitan, 10.10 Yosemite, 10.9 Mavericks
- Linux (x64) kernel 3.4 and above - CrossWorks should run on any Linux distribution that matches this criteria however we test primarily with Ubuntu distributions, this variant has been tested on all releases of Ubuntu from 12.04 LTS to 18.04 LTS

** Clang/LLVM tools and code completion not supported on this operating system.*

Computer

- 1GHz CPU or better
- 1GB RAM

- 1GB disk space free

Support

We are happy to answer any pre-sales and technical questions you have. Please submit your questions using the [CrossWorks Helpdesk](#) system.

CrossWorks for ARM

CrossWorks for ARM is a complete C/C++ and assembly language development system for **Cortex-M**, **Cortex-A**, **Cortex-R**, **ARM7**, **ARM9**, **ARM11**, and **XScale** microcontrollers. And when we say *complete we mean complete* — CrossWorks for ARM is packed full of features such as:

- **CrossStudio Integrated Development Environment.** This is our own, natively built, IDE which takes care of edit, build, download, and debugging over SWD/JTAG.
- GNU GCC and Clang/LLVM C/C++ toolchains.
- The **CrossWorks Debugger.** Our multi-core debugger able to seamlessly debug applications built from within the IDE or applications built with external toolchains.
- The **CrossWorks C Library.** This is our own non-GPL and non-LGPL C runtime library is designed from the ground up for embedded processor applications.
- The [CrossWorks Tasking Library](#), known as CTL. CTL is a royalty-free tasking library that provides a multi-priority, pre-emptive, task switching and synchronization facility.

[CrossWorks for ARM version 4 Frequently Asked Questions](#)

Supported cores, devices, boards, and technologies

CrossWorks supports a wide range of ARM cores:

| | | | | |
|------------|-------------------------|---|-----------------------------|-----------|
| Mainstream | Cortex-M0 Cortex-M0+ | Cortex-M1 | Cortex-M3 | Cortex-M4 |
| | Cortex-M7 | Cortex-M23 | Cortex-M33 | |
| Advanced | Cortex-A5 | Cortex-A7 | Cortex-A8 | Cortex-A9 |
| | Cortex-A15 | Cortex-A53 | Cortex-R4F | Cortex-R5 |
| Mature | ARM7TDMI-S | ARM920T ARM926EJ-S ARM946-E-S ARM966E-S ARM968E-S | ARM1136JF-S ARM1176JZF-S | XScale |

For a complete list of the CPUs, devices, and technologies we support, you can browse...

- [Supported Devices](#) for a list of supported devices.
- [CPU Support packages](#) for all popular, and not-so-popular microcontrollers.
- [Board Support packages](#) to start with known-good hardware and tested supporting software.

Target Connections

CrossWorks supports a wide range of adapters to program and debug your target:



Our [CrossConnect for ARM](#) JTAG/SWD adapter is the ideal interface to use with CrossWorks. It supports ARM7, ARM9, XScale, and Cortex cores and you can use it with Windows, macOS, and Linux versions of CrossWorks. What's more, it comes with a format converter for SWD featuring both 20-pin ARM and 10-pin Cortex connections.



The SEGGER [J-Link](#) is one of the most popular debug probes available, take a look at [SEGGER Microcontroller](#).



CMSIS-DAP compatible devices such as the Keil [ULINK2](#) and NXP [LPC-LINK 2](#). CMSIS-DAP is now integrated onto many evaluation boards such as mbed and Seeed Arch Pro.



STMicroelectronics [ST-LINK](#) and [ST-LINK/V2](#) devices. As with CMSIS-DAP, ST-Links are now integrated onto evaluation boards in the DISCOVERY and Nucleo ranges, including the professional ST 'EVAL' evaluation boards.



FTDI Chip FT2232/FT4232 based JTAG devices such as the Amontec JTAGkey and JTAGkey-Tiny, Olimex [ARM-USB-OCD](#) and [ARM-USB-TINY](#).
[Licensing Do](#)