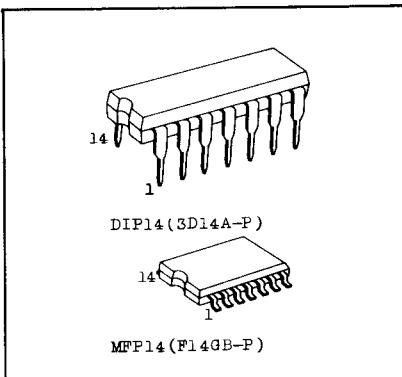


TC4001BP/TC4001BF QUAD 2 INPUT NOR GATE  
TC4002BP/TC4002BF DUAL 4 INPUT NOR GATE  
TC4025BP/TC4025BF TRIPLE 3 INPUT NOR GATE

The TC4001BP/BF, the TC4025BP/BF and TC4002BP/BF are 2-input, 3-input, 4-input positive NOR gate, respectively.

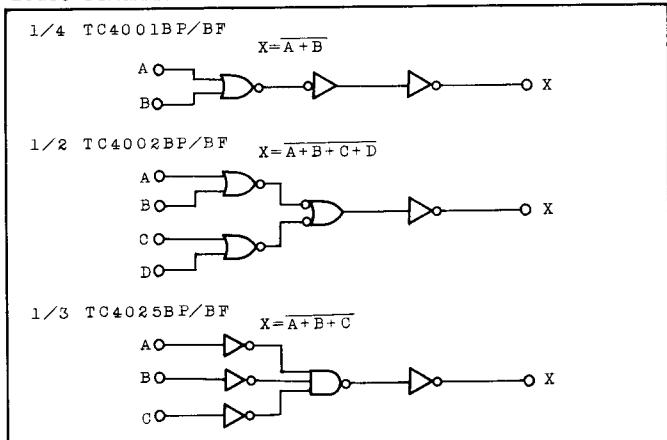
Since the outputs of these gates are equipped with the buffers, the input/output transmission characteristics have been improved and the variation of transmission time due to an increase in the load capacity is kept minimum.



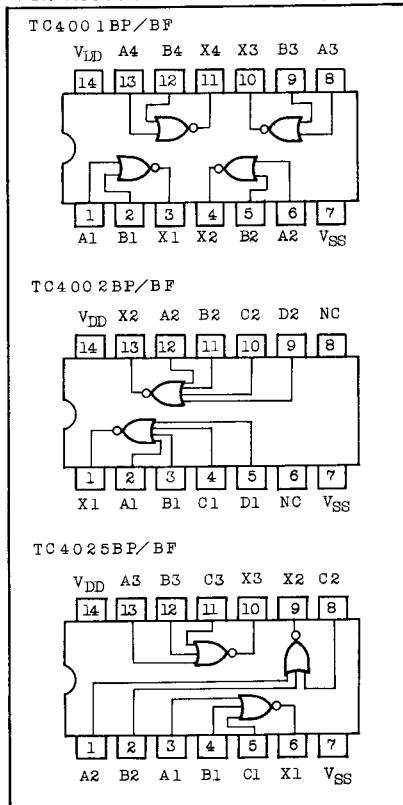
#### ABSOLUTE MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	V <sub>DD</sub>	V <sub>SS</sub> -0.5 ~ V <sub>SS</sub> +20	V
Input Voltage	V <sub>IN</sub>	V <sub>SS</sub> -0.5 ~ V <sub>DD</sub> +0.5	V
Output Voltage	V <sub>OUT</sub>	V <sub>SS</sub> -0.5 ~ V <sub>DD</sub> +0.5	V
DC Input Current	I <sub>IN</sub>	±10	mA
Power Dissipation	P <sub>D</sub>	300(DIP)/180(MFP)	mW
Operating Temperature Range	T <sub>A</sub>	-40 ~ 85	°C
Storage Temperature Range	T <sub>stg</sub>	-65 ~ 150	°C
Lead Temp./Time	T <sub>sol</sub>	260°C • 10 sec	

#### LOGIC DIAGRAM



#### PIN ASSIGNMENT (TOP VIEW)



# TC4001BP/BF, TC4002BP/BF, TC4025BP/BF

## RECOMMENDED OPERATING CONDITIONS ( $V_{SS}=0V$ )

CHARACTERISTICS	SYMBOL		MIN.	TYP.	MAX.	UNITS
DC Supply Voltage	$V_{DD}$		3	-	18	V
Input Voltage	$V_{IN}$		0	-	$V_{DD}$	V

## STATIC ELECTRICAL CHARACTERISTICS ( $V_{SS}=0V$ )

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	$V_{DD}$ (V)	-40°C		25°C		85°C		UNITS
				MIN.	MAX.	MIN.	TYP.	MAX.	MIN.	
High-Level Output Voltage	$V_{OH}$	$ I_{OUT}  < 1\mu A$ $V_{IN}=V_{SS}, V_{DD}$	5	4.95	-	4.95	5.00	-	4.95	V
			10	9.95	-	9.95	10.00	-	9.95	
Low-Level Output Voltage	$V_{OL}$	$ I_{OUT}  < 1\mu A$ $V_{IN}=V_{SS}, V_{DD}$	5	-	0.05	-	0.00	0.05	-	0.05
			10	-	0.05	-	0.00	0.05	-	
Output High Current	$I_{OH}$	$V_{OH}=4.6V$ $V_{OH}=2.5V$ $V_{OH}=9.5V$ $V_{OH}=13.5V$ $V_{IN}=V_{SS}$	5	-0.61	-	-0.51	-1.0	-	-0.42	mA
			5	-2.5	-	-2.1	-4.0	-	-1.7	
Output Low Current	$I_{OL}$	$V_{OL}=0.4V$ $V_{OL}=0.5V$ $V_{OL}=1.5V$ $V_{IN}=V_{SS}, V_{DD}$	5	0.61	-	0.51	1.2	-	0.42	-
			10	1.5	-	1.3	3.2	-	1.1	
Input High Voltage	$V_{IH}$	$V_{OUT}=0.5V$ $V_{OUT}=1.0V$ $V_{OUT}=1.5V, 13.5V$ $ I_{OUT}  < 1\mu A$	5	3.5	-	3.5	2.75	-	3.5	V
			10	7.0	-	7.0	5.5	-	7.0	
Input Low Voltage	$V_{IL}$	$V_{OUT}=0.5V, 4.5V$ $V_{OUT}=1.0V, 9.0V$ $V_{OUT}=1.5V, 13.5V$ $ I_{OUT}  < 1\mu A$	5	-	1.5	-	2.25	1.5	-	1.5
			10	-	3.0	-	4.5	3.0	-	
Input Current "H" Level	$I_{IH}$	$V_{IH}=18V$	5	-	0.1	-	$10^{-5}$	0.1	-	1.0
			10	-	-0.1	-	$-10^{-5}$	-0.1	-	
Input Current "L" Level	$I_{IL}$	$V_{IL}=0V$	5	-	0.25	-	0.001	0.25	-	-1.0
			10	-	0.5	-	0.001	0.5	-	
Quiescent Device Current	$I_{DD}$	$V_{IN}=V_{SS}, V_{DD}$ *	5	-	0.25	-	0.002	1.0	-	7.5
			10	-	0.5	-	0.002	1.0	-	
			15	-	1.0	-	0.002	1.0	-	
			*	-	-	-	-	-	-	

\* All valid input combinations.

# TC4001BP/BF, TC4002BP/BF, TC4025BP/BF

DYNAMIC ELECTRICAL CHARACTERISTICS (Ta=25°C, V<sub>SS</sub>=0V, C<sub>L</sub>=50pF)

CHARACTERISTIC	SYMBOL	TEST CONDITION	V <sub>DD</sub> (V)	MIN.	TYP.	MAX.	UNITS
Output Transition Time (TC4002BP/BF)	$t_{TLH}$		5	-	80	200	ns
			10	-	50	100	
			15	-	40	80	
Output Transition Time (TC4002BP/BF)	$t_{THL}$		5	-	80	200	ns
			10	-	50	100	
			15	-	40	80	
Output Transition Time (TC4001BP/BF), (TC4025BP/BF)	$t_{TLH}$		5	-	70	200	ns
			10	-	35	100	
			15	-	30	80	
Output Transition Time (TC4001BP/BF), (TC4025BP/BF)	$t_{THL}$		5	-	70	200	ns
			10	-	35	100	
			15	-	30	80	
Propagation Delay Time (TC4001BP/BF)	$t_{pLH}$		5	-	65	200	ns
			10	-	30	100	
			15	-	25	80	
Propagation Delay Time (TC4001BP/BF)	$t_{pHL}$		5	-	65	200	ns
			10	-	30	100	
			15	-	25	80	
Propagation Delay Time (TC4002BP/BF)	$t_{pLH}$		5	-	100	250	ns
			10	-	40	120	
			15	-	30	90	
Propagation Delay Time (TC4002BP/BF)	$t_{pHL}$		5	-	100	250	ns
			10	-	40	120	
			15	-	30	90	
Propagation Delay Time (TC4025BP/BF)	$t_{pLH}$		5	-	70	200	ns
			10	-	35	100	
			15	-	30	80	
Propagation Delay Time (TC4025BP/BF)	$t_{pHL}$		5	-	70	200	ns
			10	-	35	100	
			15	-	30	80	
Input Capacitance	C <sub>IN</sub>			-	5	7.5	pF

CIRCUIT AND WAVEFORM FOR MEASUREMENT OF DYNAMIC CHARACTERISTICS

