

KR580VB55

PA3	1	40	PA4
PA2	2	39	PA5
PA1	3	38	PA6
PA0	4	37	PA7
\overline{RD}	5	36	\overline{WR}
\overline{CS}	6	35	RESET
GND	7	34	D0
A1	8	33	D1
A0	9	32	D2
PC7	10	31	D3
PC6	11	30	D4
PC5	12	29	D5
PC4	13	28	D6
PC0	14	27	D7
PC1	15	26	Vcc
PC2	16	25	PB7
PC3	17	24	PB6
PB0	18	23	PB5
PB1	19	22	PB4
PB2	20	21	PB3

KP580BB55 (functional analog of [Intel 8255](#)) - an electronic component, a [chip of a](#) programmable controller of parallel input-output. The microcircuit allows you to address the data bus on three separate channels, another channel is used as the control register of the microcircuit.

Application

The microcircuit allows you to address the signal from the data bus to three external objects using three 8-bit data channels (PortA, PortB, PortC), which can operate both input and output. The operation mode of each channel is set by the control word, which is supplied to the device register with the OUT command. PortA and PortB can work either input or output at the same time. PortC is presented as two 4-bit ports, and each tetrad of it can independently be connected to input or output. In addition to three 8-bit data channels, the chip has an 8-bit channel for connecting to the data bus, as well as two address inputs that allow one of 4 addresses to be implemented: the choice of one of the three data channels or the device register.