

MITSUBISHI LSTTLs M74LS279P

QUADRUPLE R-S LATCH

DESCRIPTION

The M74LS279P is a semiconductor integrated circuit containing 4 R-S flip-flop circuits.

FEATURES

- High breakdown input voltage ($V_I \geq 15V$)
- High breakdown output voltage ($V_O \geq 7V$)
- Low power dissipation ($P_D = 19mW$ typical)
- Low output impedance
- Wide operating temperature range ($T_a = -20 \sim +75^\circ C$)

APPLICATION

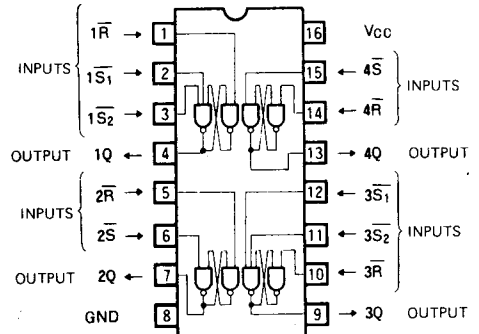
General purpose, for use in industrial and consumer equipment.

FUNCTIONAL DESCRIPTION

Two of the 4 circuits have set inputs \bar{S}_1 and \bar{S}_2 and reset input \bar{R} and the other 2 circuits have \bar{S} and \bar{R} inputs.

When \bar{S}_1 or \bar{S}_2 or both are low or \bar{S} is low, high appears in output Q, and when R is low, low appears in output Q. When \bar{S}_1 or \bar{S}_2 or both are low and \bar{R} is low, high appears in the output but when each of the inputs is set high at the same time, the status of Q cannot be anticipated.

PIN CONFIGURATION (TOP VIEW)



Outline 16P4

FUNCTION TABLE (Note 1)

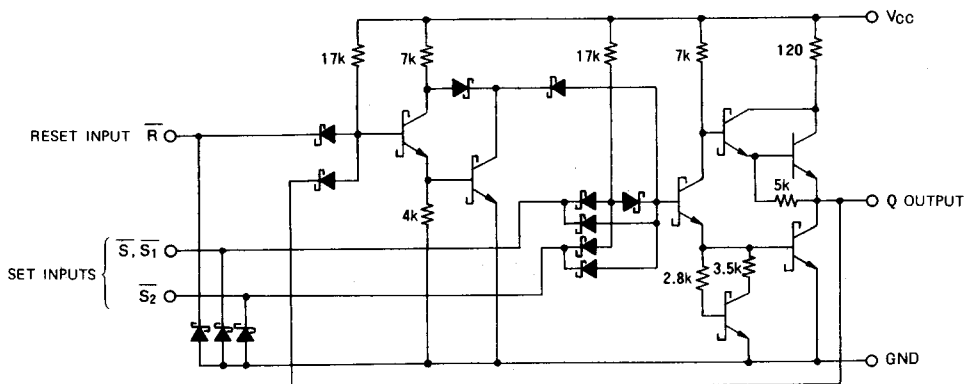
\bar{S}_1	\bar{S}_2	\bar{R}	Q
L	X	L	H*
X	L	L	H*
L	X	H	H
X	L	H	H
H	H	L	L
H	H	H	Q^0

Note 1 Q^0 : Level of Q before the indicated steady-state input conditions were established

X: Irrelevant

*: Nonstable, it will not persist when \bar{R} , \bar{S}_1 and \bar{S}_2 return to their inactive (high) level

CIRCUIT SCHEMATIC (EACH LATCH)



UNIT: Q

QUADRUPLE R-S LATCH

ABSOLUTE MAXIMUM RATINGS (Ta = -20 ~ +75°C, unless otherwise noted)

Symbol	Parameter	Conditions	Limits	Unit
V _{CC}	Supply voltage		-0.5 ~ +7	V
V _I	Input voltage		-0.5 ~ +15	V
V _O	Output voltage	High-level state	-0.5 ~ V _{CC}	V
T _{opr}	Operating free-air ambient temperature range		-20 ~ +75	°C
T _{stg}	Storage temperature range		-65 ~ +150	°C

RECOMMENDED OPERATING CONDITIONS (Ta = -20 ~ +75°C, unless otherwise noted)

Symbol	Parameter		Limits			Unit
			Min	Typ	Max	
V _{CC}	Supply voltage		4.75	5	5.25	V
I _{OH}	High-level output current	V _{OH} ≥ 2.7V	0		-400	μA
I _{OL}	Low-level output current	V _{OL} ≤ 0.4V	0		4	mA
		V _{OL} ≤ 0.5V	0		8	mA

ELECTRICAL CHARACTERISTICS (Ta = -20 ~ +75°C, unless otherwise noted)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ *	Max	
V _{IH}	High-level input voltage		2			V
V _{IL}	Low-level input voltage				0.8	V
V _{IC}	Input clamp voltage	V _{CC} = 4.75V, I _{IC} = -18mA			-1.5	V
V _{OH}	High-level output voltage	V _{CC} = 4.75V, V _I = 0.8V V _I = 2V, I _{OH} = -400μA	2.7	3.4		V
V _{OL}	Low-level output voltage	V _{CC} = 4.75V		0.25	0.4	V
		V _I = 0.8V, V _I = 2V		0.35	0.5	V
I _{IH}	High-level input current	V _{CC} = 5.25V			20	μA
		V _I = 2.7V				
		V _{CC} = 5.25V			0.1	mA
I _{IL}	Low-level input current	V _{CC} = 5.25V			-0.4	mA
		V _I = 0.4V				
I _{OS}	Short-circuit output current (Note 2)	V _{CC} = 5.25V, V _O = 0V	-20		-100	mA
I _{CC}	Supply current	V _{CC} = 5.25V (Note 3)		3.8	7	mA

* : All typical values are at V_{CC} = 5V, Ta = 25°C

Note 2: All measurements should be done quickly and not more than one output should be shorted at a time.

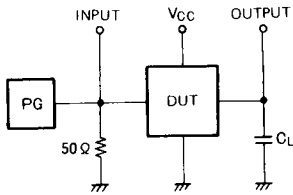
Note 3: I_{CC} is measured with all R inputs at 0V and all S inputs at 4.5V.

SWITCHING CHARACTERISTICS (V_{CC} = 5V, Ta = 25°C, unless otherwise noted)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
t _{PLH}	Low-to-high-level, high-to-low-level output propagation time, from input S to output Q	C _L = 15pF (Note 4)		6	22	ns
t _{PHL}				12	21	ns
t _{PHL}	High-to-low-level output propagation time, from input R to output Q			12	27	ns

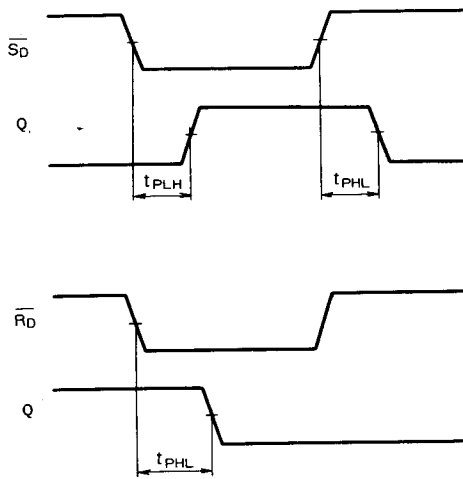
QUADRUPLE R-S LATCH

Note 4: Measurement circuit



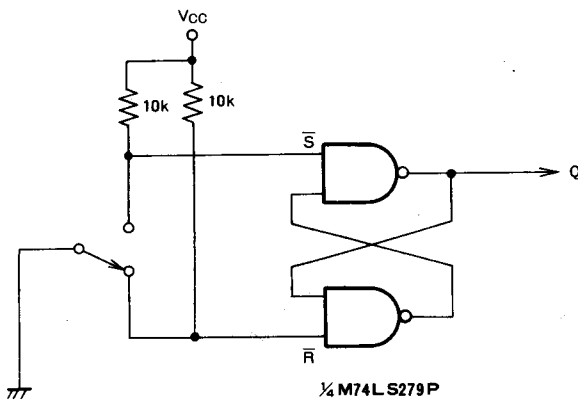
- (1) The pulse generator (PG) has the following characteristics:
 PRR = 1MHz, $t_r = 6ns$, $t_f = 6ns$, $t_w = 500ns$,
 $V_p = 3V_{p-p}$, $Z_0 = 50\Omega$.
- (2). C_L includes probe and jig capacitance

TIMING DIAGRAM (Reference level = 1.3V)



APPLICATION EXAMPLE

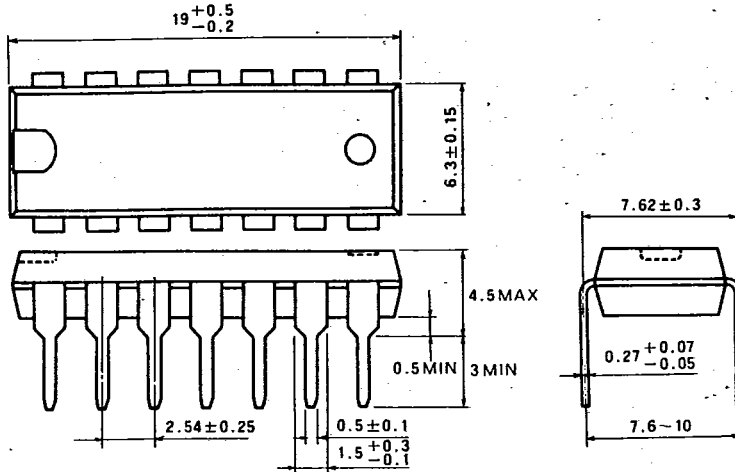
Chattering prevention circuit



T-90-20

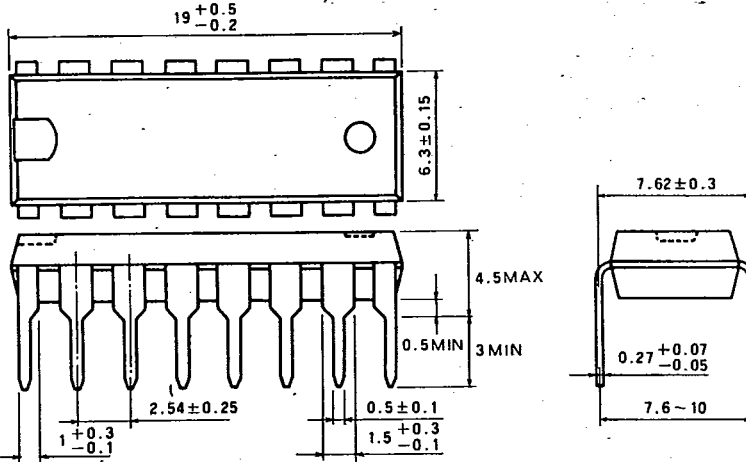
TYPE 14P4 14-PIN MOLDED PLASTIC DIL

Dimension in mm



TYPE 16P4 16-PIN MOLDED PLASTIC DIL

Dimension in mm



TYPE 20P4 20-PIN MOLDED PLASTIC DIL

Dimension in mm

