

TM084SDHG01 V1.0

MODEL NO. : <u>TM084SDHG01</u>

ISSUED DATE: 2012-05-23

VERSION : Ver 1.0

Preliminary Specification □Final Product Specification

<u> </u>	
Customer	
CUSIOMEI	

Approved by	Notes

SHANGHAI TIANMA Confirmed :

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This technical specification is subjected to change without notice

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Record of Revision

Rev	Issued Date	Description	Editor
1.0	2012-5-23	Preliminary Specification	Longping.Deng

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1 General Specifications

	Feature	Spec
	Size	8.4 inch
	Resolution	800(RGB) x 600
	Interface	LVDS 6/8 bit
	Color Depth	16.7M
	Technology Type	a-Si TFT
Display Spec.	Pixel Pitch (mm)	0.213 ×0.213
	Pixel Configuration	R.G.B. Vertical Stripe
	Display Mode	TM,NW
	Surface Treatment	Anti Glare
	Viewing Direction	6 o'clock
	Gray Scale Inversion Direction	12 o'clock
	LCM (W x H x D) (mm)	203x 142.5 x 5.7
	Active Area(mm)	170.4 (W) X127.8(H)
Mechanical Characteristics	With /Without TSP	Without TSP
	Weight (g)	TBD
	LED Numbers	27 LEDS

Note 1: Viewing direction for best image quality is different from TFT definition. There is a 180 degree shift.

- Note 2: Requirements on Environmental Protection: Q/S0002
- Note 3: LCM weight tolerance: +/- 5%

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2 Input/Output Terminals

2.1 CN1 of FPC

Connector type: 107A20-0021RA-G3-R (Hirose)

Pin	Symbol	I/O	Description	Remark
1	VDD	Р	Power supply	
2	VDD	Р	Power supply	
3	UD	I	Vertical Reverse Scan Control	
4	LR	I	Horizontal Reverse Scan Control	
5	RxIN1-	I	Negative data 1 for LVDS signal input	
6	RxIN1+	I	Positive data 1 for LVDS signal input	
7	GND	Р	Ground	
8	RxIN2-	1	Negative data 2 for LVDS signal input	
9	RxIN2+	1	Positive data 2 for LVDS signal input	
10	GND	Р	Ground	
11	RxIN3-	I	Negative data 3 for LVDS signal input	
12	RxIN3+	I	Positive data 3 for LVDS signal input	
13	GND	Р	Ground	
14	RxCLKIN-	I	negative clock for LVDS signal input	
15	RxCLKIN+	I	Positive clock for LVDS signal input	
16	GND	Р	Ground	
17	SEL68	I	LVDS 6/8 bit selection control	
18	NC	1	No connection	
19	RxIN4-	I	Negative data 4 for LVDS signal input	
20	RxIN4+		Positive data 4 for LVDS signal input	

I---Input, O---Output, P--- Power/Ground

Table 2.1 terminal pin assignments

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2.2 CN2 pin assignment (Backlight interface)

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Connector type: 3808K-F04N-02R (Entery)

Pin	Symbol	I/O	Description	Remark
1	VBL	Р	Backlight Power supply , 12V input	
2	PGND	Р	Ground	
3	BL_EN	Ι	Enable : 3.3V for backlight on ; 0V for backlight off	
4	Dimming	Ι	Adjust the luminance of LED's	

Note1: CN2 Matching Connector type: H208K-P04N-02B (Entery)

 Table 2.2 Backlight terminal pin assignments

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SHANGHAI TIANMA MICRO-ELECTRONICS Absolute Maximum Ratings

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GND=0V, Ta = 25℃

Item	Symbol	MIN	MAX	Unit	Remark
	VDD	-0.5	5.0	V	
Power Voltage	VBL	-0.3	24	V	
Input voltage	V _{IN}	-0.5	5.0	V	Note2
Operating Temperature	Тор	-20	70	°C	Note1
Storage Temperature	Tst	-30	80	°C	Note1

Note1: The parameter is for driver IC (gate driver, source driver) only. Note2: Signals include RxIN, RxCLK, SEL68, UD, LR ,EN ,Dimming.

Table 3.1 absolute maximum rating

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4 Electrical Characteristics

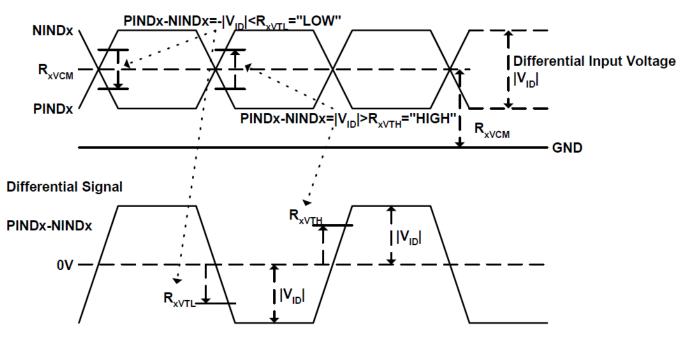
4.1 Recommended Operating Condition

	VCC=3.3V, GND=0V, Ta = 25℃					
Item	Symbol	Min	Тур	Мах	Unit	Remark
POWER Supply Voltage	VDD	3.00	3.30	3.60	V	
Differential input high threshold voltage	R _x vтн			+0.1	V	R _{xVCM} =1.2V
Differential input low threshold voltage	R xvtl	-0.1			V	
Input voltage range (singled-end)	RXVIN	0		2.4	V	
Differential input common mode voltage	R хvсм	Vıd /2		2.4- V _{ID} /2	V	
Differential input voltage	V ID	0.2		0.6	٧	
Current of VDD Power supply	Ivdd	-	TBD	TBD	mA	Note1
Power consumption of VDD	Wvdd		TBD	-	mW	

Table 4.1 LCD module electrical characteristics

Note1: To test the current dissipation, use "all Black Pattern".

Single-end Signals



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4.2 Backlight Unit Driving Condition

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LEDGND=GND=0V, Ta = 25℃						Ta = 25℃	
Item		Symbol	Min	Тур	Мах	Unit	Remark
Voltage o	f LED driver circuit	VBL	11.5	12	12.5	V	
Current of LED driver circuit		IBL	-	TBD	-	mA	
Power Consumption		W _{BL}	-	TBD	-	mW	
Dimming Sig	Dimming Signal frequency		0.1		100	KHz	
Dimming	100Hz~10KHz	-	0	-	100	%	
Signal duty	10KHz~100KHz	-	10	-	100	%	
LED Life time	9	-		(50000)		Н	

Table 4.2 LED backlight characteristics

Note 1: If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data

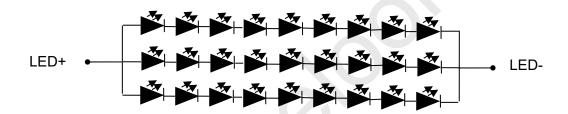


Figure 4.1 LED connection of backlight

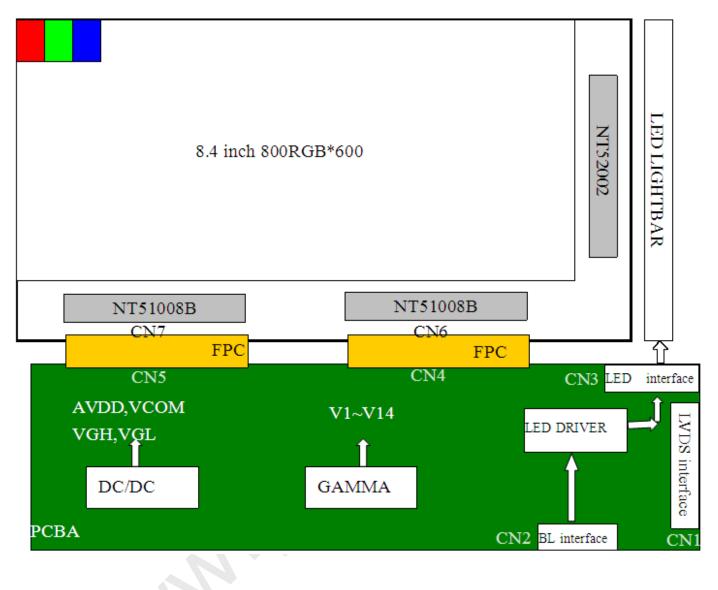
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4.3 BLOCK DIAGRAM



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5 Timing Chart

5.1 TFT-LCD Input Timing

VCC=3.3V, GND=0V, Ta=25 °C						
Parameter	Symbol	Min	Тур	Max	Unit	Remark
	Fclk	34.5	39.6	50.4	MHz	Tclk=1/Fclk
DCLK	Tclk	16	25.3	30.7	ns	
	TH	900	1000	1200	Tclk	
	THD	-	800	-	Tclk	
HSYNC	Thwh	1	-	40	Tclk	
	Thbp	-	88	-	Tclk	
	Thfp	12	112	312	Tclk	
	TV	640	660	700	TH	
	TVD	-	480	-	ΤΗ	
VSYNC	Tvwh	1	-	20	ТН	
	Tvbp	-	39	-	ΤΗ	
	Tvfp	1	21	61	ТН	

 Table 5.1 TCON input timing(HV mode)

5.2 Recommended Timing Setting Of TCON TCON (Embedded In Source IC) Input timing at DE mode VCC=3.3V, GND=0V, Ta=25℃

-							
	Parameter	Symb ol	Min.	Тур.	Max.	Unit	Remark
Dclk frequency		Fclk	32.6	39.6	62.4	MHz	Tclk=1/Fclk
	Horizontal total	ТН	890	1000	1300	Tclk	
Horizontal section	Horizontal blanking	THC	90	200	500	Tclk	
	Valid Data Width	THD	-	800	-	Tclk	
	Frame rate	-	-	60	-	Hz	
Vertical section	Vertical total	TV	610	660	800	ТН	
	Vertical blanking	TVC	10	60	200	ТН	
	Valid Data Width	TVD	-	600	-	ТН	

Table 5.2 TCON input timing (DE mode)

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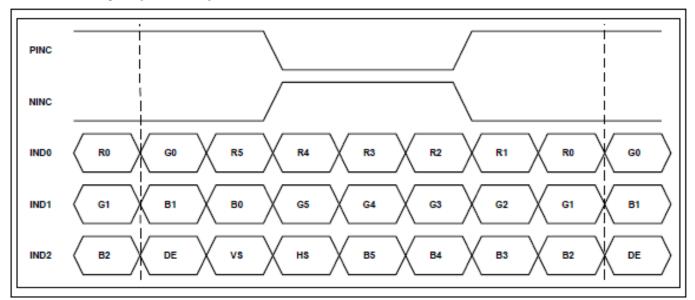
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5.3 LVDS Input Data Format

6bit LVDS input (HSD='H')



8-bit LVDS input (HSD='L')

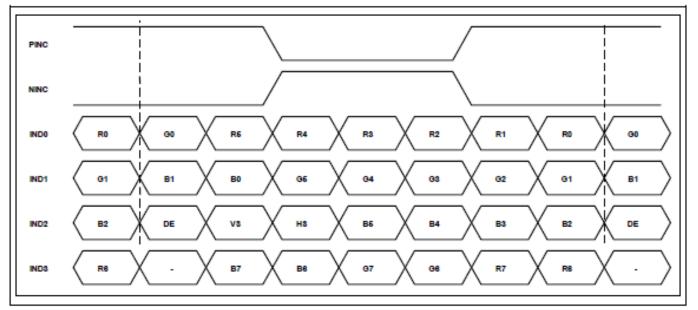


Figure 5.1 LVDS input data mapping

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5.4 Input signal AC timing

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Clock frequency	RxFCLK	32.6	39.6	62.4	MHz	
Clock high time	Тілсн		4/(7* R xfclk)		nS	
Clock low time	TLVCL		3/(7* R xfclk)		nS	

Table 5.3 LVDS clock Input timming

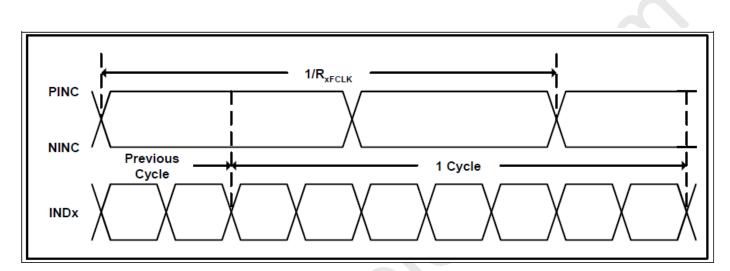


Figure 5.2 Input signal AC timing

5.5 POWER ON/OFF SEQUENCE

Item	Symbol	Min	Тур	Max	Unit	Remark
VDD 3.3V to signal starting	Tp1	5	-	50	ms	
VDD rising time	Tr	0.1	-	5	ms	
Signal starting to backlight on	Tp2	150	-	-	ms	
Signal off to VDD 0V	Тр3	5	-	50	ms	
Backlight off to signal off	Tp4	150	-	-	ms	

Table 5.4 POWER ON/OFF SEQUENCE

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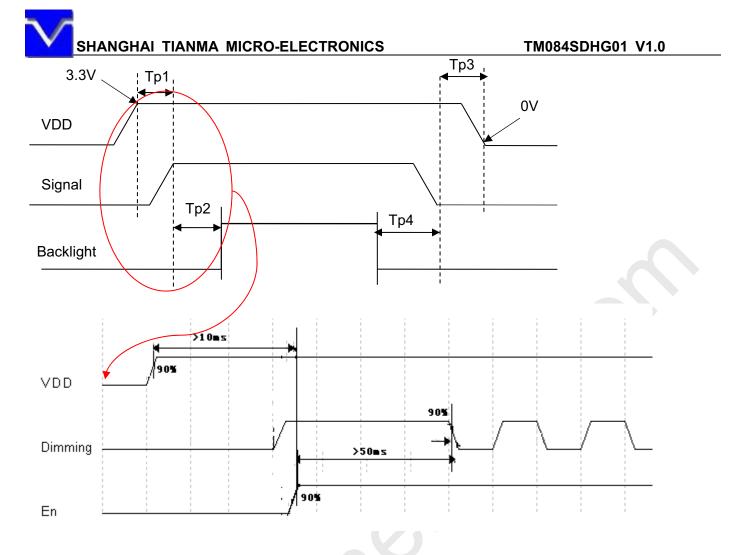


Figure 5.3 Interface power on/off sequence

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6 **Optical Characteristics**

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								Ta=
ltem	I	Symbol	Condition	Min	Тур	Max	Unit	Remark
		θΤ		50	60	-		
View Angles		θΒ	CR≧10	60	70	-	Degree	Note 2
View Angles		θL	GR≦10	60	70	-		
		θR		60	70	-		
Contrast Ratio	D	CR	θ=0°	400	500	-		Note1 Note3
		T _{ON}					-	
Response Time		TOFF	25 ℃	-	25	-	ms	Note1 Note4
	White	x		0.260	0.310	0.360	- 1	Note1 Note5
		у	Backlight is	0.280	0.330	0.380		
	Red	x		TBD	TBD	TBD		
		у		TBD	TBD	TBD		
Chromaticity	Green	x	on	TBD	TBD	TBD		
		У		TBD	TBD	TBD		
		x		TBD	TBD	TBD		
	Blue	У		TBD	TBD	TBD		
Uniformity		U		-	75	-	%	Note1 Note6
NTSC				-	50	-	%	Note 5
Luminance(Without TP)		L	5	200	250	-	cd/m ²	Note1 Note7

Test Conditions:

- 1. I_F= 160 mA, V_F=9.6 V and the ambient temperature is $25\pm2^{\circ}$.humidity is $65\pm7\%$
- 2. The test systems refer to Note 1 and Note 2.

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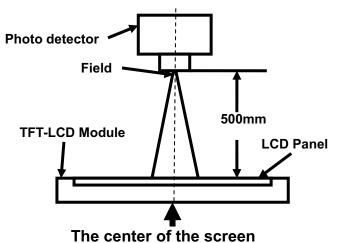
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Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 Minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.

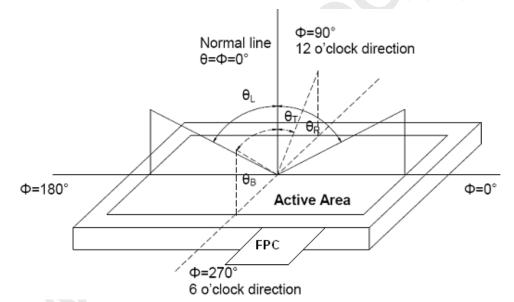


Item	Photo detector	Field
Contrast Ratio		
Luminance	SR-3A	1°
Chromaticity	SK-JA	1
Lum Uniformity		r
Response Time	BM-7A	2°
L		

Note

2: Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).



Note 3: Definition of contrast ratio

Contrast ratio (CR) = Luminance measured when LCD is on the "White" state Luminance measured when LCD is on the "Black" state

"White state ": The state is that the LCD should drive by Vwhite.

"Black state": The state is that the LCD should drive by Vblack.

Vwhite: To be determined Vblack: To be determined.

Note 4: Definition of Response time

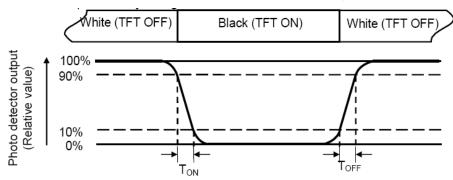
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The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)

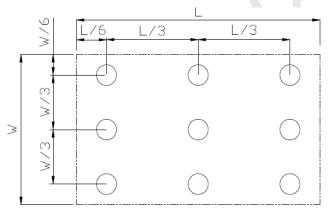
Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (U) = Lmin/ Lmax

L-----Active area length W----- Active area width



Lmax: The measured Maximum luminance of all measurement position.

Lmin: The measured Minimum luminance of all measurement position.

Note 7: Definition of Luminance:

Measure the luminance of white state at center point.

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7 Environmental / Reliability Test

No	Test Item	Condition	Remarks
1	High Temperature Operation	Ts = +70℃, 240 hours	IEC60068-2-1 GB2423.2
2	Low Temperature Operation	Ta = -20℃, 240 hours	IEC60068-2-1 GB2423.1
3	High Temperature Storage	Ta = +80℃, 240 hours	IEC60068-2-1 GB2423.2
4	Low Temperature Storage	Ta = -30℃, 240 hours	IEC60068-2-1 GB2423.1
5	Storage at High Temperature and Humidity	Ta = +60℃, 90% RH max,240hours	IEC60068-2-78 GB/T2423.3
6	Thermal Shock (non-operation)	-30℃ 30 min~+80℃ 30 min, Change time:5min, 100 Cycle	Start with cold temperature, End with high temperature, IEC60068-2-14,GB2423.22
7	ESD	C=150pF,R=330Ω,5point/panel Air:±8Kv,5times; Contact:±4Kv,5times (Environment:15℃~35℃, 30%~60%.86Kpa~106Kpa)	IEC61000-4-2 GB/T17626.2
8	Vibration Test	Frequency range:10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z. (6 hours for total)	IEC60068-2-6 GB/T2423.10
9	Mechanical Shock (Non Op)	Half Sine Wave 100G 6ms, ±X,±Y,±Z 3times for each direction	IEC60068-2-27 GB/T2423.5
10	Package Drop Test	Height:60cm, 1corner,3edges,6surfaces	IEC60068-2-32 GB/T2423.8

Note1: Ts is the temperature of panel's surface.

Note2: Ta is the ambient temperature of samples.

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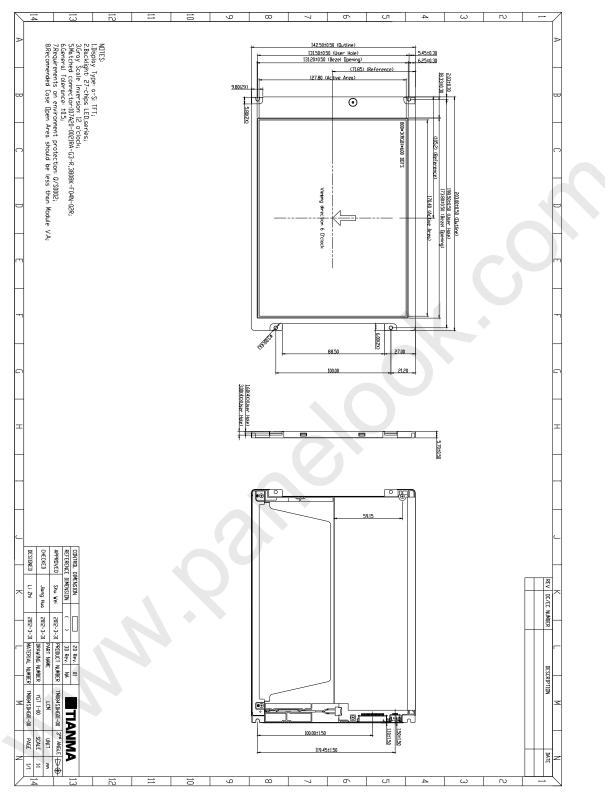
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8 Mechanical Drawing



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SHANGHAI TIANMA MICRO-ELECTRONICS Packing drawing

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				-			
No	Item	Model (Material)	Dimensions(mm)	Unit Weight(Kg)	Quantity	Remark	
1	LCM module	TM084SDHG01-00	203X142.5X5.7	TBD	36		
2	Partition_1	Corrugated paper	527X348X217	1.571	1		
3	Anti-static Bag	PE	161X253X0.05	0.001	36	Anti-static	
4	Dust-Proof Bag	PE	700X545	0.06	1		
5	Partition_2	Corrugated Paper	505X332X4.0	0.92	2		
6	Corrugated Bar	Corrugated paper	348X173	0.057	4		
7	Carton	Corrugated paper	544X365X250	0.76	1		
9	Total weight	TBD Kg±10%					

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SHANGHAI TIANMA MICRO-ELECTRONICS 10 Precautions for Use of LCD Modules

10.1 Handling Precautions

10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5 If the display surface is contaMinated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

Isopropyl alcohol

- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

Water

Ketone

Aromatic solvents

10.1.6 Do not attempt to disassemble the LCD Module.

10.1.7 If the logic circuit power is off, do not apply the input signals.

10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

10.1.8.1 Be sure to ground the body when handling the LCD Modules.

10.1.8.2 Tools required for assembly, such as soldering irons, must be properly ground.

10.1.8.3 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

10.1.8.4 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage precautions

10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0° C $\sim 40^{\circ}$ C Relatively humidity: $\leq 80^{\circ}$

10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.3 Transportation Precautions

10.3.1 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

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