

## Silvaco



<b>Type</b>	Private Company
<b>Industry</b>	<a href="#">Software &amp; Programming</a>
<b>Founded</b>	1984
<b>Founder</b>	Ivan Pesic
<b>Headquarters</b>	<a href="#">Santa Clara, California</a>
<b>Key people</b>	Iliya Pesic, Chairman Babak Taheri, CEO
<b>Website</b>	<a href="http://silvaco.com">silvaco.com</a>

**Silvaco Inc.** develops and markets [electronic design automation \(EDA\)](#) and [technology CAD \(TCAD\)](#) software and [semiconductor design IP \(SIP\)](#). The company is headquartered in [Santa Clara, California](#), and has a global presence with offices located in North America, Europe, and throughout Asia. Since its founding in 1984, Silvaco has grown to become a large privately held EDA company. The company has been known by at least two other names: Silvaco International,<sup>[1]</sup> and Silvaco Data Systems.<sup>[2]</sup>



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## History

Founded by Dr. Ivan Pesic (September 13, 1951, [Resnik, Montenegro](#) — October 20, 2012, Japan)<sup>[3][4]</sup> in 1984, the company is privately held and internally funded. It is headquartered in [Santa Clara, California](#), with fourteen offices worldwide.

In 2003 Silvaco acquired Simucad Inc., a privately held company providing logic simulation EDA software.<sup>[5]</sup> Silvaco re-launched the brand by spinning out its EDA product line in 2006 under the Simucad name.<sup>[6][7]</sup> As of February 17, 2010, Simucad Design Automation and Silvaco Data Systems were merged back together forming Silvaco, Inc.<sup>[8]</sup>

In 2006,<sup>[9]</sup> Silvaco sued [Intel](#) for misappropriation of trade secrets in the case of [Silvaco Data Systems v. Intel Corp.](#), however ultimately the judgment of the Court was in favor of Intel.

In 2012, David Halliday was appointed CEO after the death of the company founder Ivan Pesic.<sup>[3]</sup>

In 2015, Silvaco appointed a new CEO, David Dutton.<sup>[10]</sup> The company also acquired Invarian, Inc., a privately held company providing [power integrity](#) analysis software,<sup>[11]</sup> and acquired Infiniscale SA, a privately held company in France providing variability analysis software.<sup>[12]</sup>

In 2016, Silvaco added [semiconductor design IP](#) (SIP) to its portfolio with the acquisition of the privately held company IPextreme, Inc.<sup>[13]</sup> Silvaco also entered into another new market segment with the acquisition of the privately held company edXact in France.<sup>[14]</sup> The tools from edXact are used for analysis, reduction, and comparison of extracted parasitic netlists.

In 2017, Silvaco acquired SoC Solutions, a privately held company providing semiconductor IP.<sup>[15]</sup>

In 2018, Silvaco acquired [NanGate](#), a privately held company providing tools and services for creation, optimization, characterization, and validation of physical library IP.<sup>[16]</sup> The company also announced a partnership with [Purdue University](#) and the Purdue Research Foundation for the commercialization of the NEMO tool suite, an atomistic nanoelectronics modeling and simulation tool.<sup>[17]</sup>

In 2019, Silvaco appointed Dr. Babak Taheri as new Chief Executive Officer.<sup>[18]</sup>

## Products

Silvaco delivers EDA and semiconductor [TCAD](#) software products and semiconductor design IP with support and engineering services. Worldwide customers include leading foundries, fabless semiconductor companies, OEMs, integrated semiconductor manufacturers, and universities.

### TCAD Products

#### Process Simulation

- Victory Process - 2D/3D process simulator

## Device Simulation

- Victory Device 2D/3D device simulator

## Other tools

- Virtual Wafer Fab (VWF) - Emulation of wafer manufacturing to perform design-of-experiments and optimization.

## EDA Products

The company supplies integrated [EDA](#) software in the areas of analog/mixed-signal/RF circuit simulation, custom IC CAD, interconnect modeling, and standard cell library development and characterization.

### SPICE modeling and analog & mixed-signal simulation

- Utmost IV - Device characterization and SPICE modeling
- [SmartSpice](#) - Analog circuit simulator
- SmartSpice RF - Frequency and time domain RF circuit simulator
- SmartView - Simulation waveform viewer

### Custom IC CAD

- Gateway - Schematic editor
- Expert - Layout editor
- Guardian - DRC/LVS/Net physical verification
- Hipex - Full-chip parasitic extraction
- Jivaro - Parasitic reduction and analysis
- VarMan - High-sigma variability analysis

### Interconnect Modeling

- Clever - Parasitic extractor for realistic 3D structures

### Library Platform

- Cello - Standard cell library creation, migration and optimization
- Viola - Standard cell library and I/O cell characterization
- Liberty Analyzer - Analysis and validation of timing, power, noise, and area data from characterization

## SIP Products

The company markets a wide variety of design IP under its SIPware brand.<sup>[19]</sup> In May 2019, the company announced that the semiconductor design IP of [Samsung Foundry](#) (SF) is now marketed, licensed, and supported through Silvaco.<sup>[20]</sup> The initial offering of hard design IP from

SF is for the 14 nm process node. The company also develops and markets standard cell library foundation IP for process nodes from 180 nm to below 14 nm. Categories of SIPware IP include:

- Interface PHYs
- Interface controllers
- Automotive controllers
- AMBA IP cores and subsystems
- Security cores
- Analog cores
- Embedded processors
- Analog front-ends and codecs
- Foundation IP