

BFU590G NPN wideband silicon RF transistor Rev. 1 — 28 April 2014

Product data sheet

1. **Product profile**

1.1 General description

NPN silicon microwave transistor for high speed, medium power applications in a plastic, 4-pin SOT223 package.

The BFU590G is part of the BFU5 family of transistors, suitable for small signal to medium power applications up to 2 GHz.

1.2 Features and benefits

- Medium power, high linearity, high breakdown voltage RF transistor
- AEC-Q101 qualified
- Maximum stable gain 13 dB at 900 MHz
- P_{L(1dB)} 21.5 dBm at 900 MHz
- 8.5 GHz f_T silicon technology

1.3 Applications

- Automotive applications
- Broadband amplifiers
- Medium power amplifiers (500 mW at a frequency of 433 MHz or 866 MHz)
- Large signal amplifiers for ISM applications

1.4 Quick reference data

Table 1. **Quick reference data**

$T_{amb} = 25 \ ^{\circ}C$ unless otherwise specified

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|------------------|---------------------------|--|------------|-----|-----|------|------|
| V _{CB} | collector-base voltage | open emitter | | - | - | 24 | V |
| V _{CE} | collector-emitter voltage | open base | | - | - | 12 | V |
| | | shorted base | | - | - | 24 | V |
| V _{EB} | emitter-base voltage | open collector | | - | - | 2 | V |
| I _C | collector current | | | - | 80 | 200 | mA |
| P _{tot} | total power dissipation | $T_{sp} \le 90 \ ^{\circ}C$ | <u>[1]</u> | - | - | 2000 | mW |
| h _{FE} | DC current gain | I _C = 80 mA; V _{CE} = 8 V | | 60 | 95 | 130 | |
| C _c | collector capacitance | V _{CB} = 8 V; f = 1 MHz | | - | 1.9 | - | pF |
| f _T | transition frequency | I _C = 80 mA; V _{CE} = 8 V; f = 900 MHz | | - | 8.5 | - | GHz |



NPN wideband silicon RF transistor

| Table 1.Quick reference data continued $T_{amb} = 25 \ ^{\circ}C$ unless otherwise specified | | | | | | | | | |
|--|---------------------------------------|---|-----|------|---|-----|--|--|--|
| Symbol | Min | Тур | Max | Unit | | | | | |
| G _{p(max)} | maximum power gain | I _C = 80 mA; V _{CE} = 8 V; f = 900 MHz [2] | - | 13 | - | dB | | | |
| P _{L(1dB)} | output power at 1 dB gain compression | I _C = 80 mA; V _{CE} = 8 V; Z _S = Z _L = 50 Ω; f = 900 MHz | - | 21.5 | - | dBm | | | |

[1] T_{sp} is the temperature at the solder point of the collector lead.

[2] If K > 1 then $G_{p(max)}$ is the maximum power gain. If K < 1 then $G_{p(max)}$ = MSG.

Pinning information 2.

| Pin | Description | Simplified outline | Graphic symbol |
|-----|-------------|--------------------|----------------|
| 1 | emitter | | |
| 2 | base | | 4 |
| 3 | emitter | | 2 |
| 4 | collector | | 1, 3 |
| | | | mbb159 |

Ordering information 3.

Table 3. **Ordering information**

| Type number | Package |) | |
|-------------|---------|--|--------|
| | Name | ame Description | |
| BFU590G | - | plastic surface-mounted package with increased heatsink; 4 leads | SOT223 |
| OM7966 | - | Customer evaluation kit for BFU580G and BFU590G [1] | - |

[1] The customer evaluation kit contains the following:

- a) Unpopulated RF amplifier Printed-Circuit Board (PCB)
- b) Unpopulated RF amplifier Printed-Circuit Board (PCB) with emitter degeneration
- c) Four SMA connectors for fitting unpopulated Printed-Circuit Board (PCB)
- d) BFU580G and BFU590G samples
- e) USB stick with data sheets, application notes, models, S-parameter and noise files

4. Marking

Table 4. Marking

| Type number | Marking |
|-------------|---------|
| BFU590G | BFU590 |

5. Design support

Table 5.Available design support

Download from the BFU590G product information page on http://www.nxp.com.

| Support item | Available | Remarks |
|---|-----------|------------------------------------|
| Device models for Agilent EEsof EDA ADS | yes | Based on Mextram device model. |
| SPICE model | yes | Based on Gummel-Poon device model. |
| S-parameters | yes | |
| Customer evaluation kit | yes | See Section 3 and Section 10. |
| Solder pattern | yes | |
| Application notes | yes | See Section 10.1 and Section 10.2. |

6. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | Min | Max | Unit |
|------------------|---------------------------------|--|-----|------|------|
| V _{CB} | collector-base voltage | open emitter | - | 30 | V |
| V _{CE} | collector-emitter voltage | open base | - | 16 | V |
| | | shorted base | - | 30 | V |
| V _{EB} | emitter-base voltage | open collector | - | 3 | V |
| I _C | collector current | | - | 300 | mA |
| T _{stg} | storage temperature | | -65 | +150 | °C |
| V _{ESD} | electrostatic discharge voltage | Human Body Model (HBM) According to JEDEC standard 22-A114E | - | ±250 | V |
| | | Charged Device Model (CDM) According to JEDEC standard 22-C101B | - | ±2 | kV |

7. Recommended operating conditions

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|------------------|---------------------------|-------------------------|--------------|-----|------|------|
| V _{CB} | collector-base voltage | open emitter | - | - | 24 | V |
| V _{CE} | collector-emitter voltage | open base | - | - | 12 | V |
| | | shorted base | - | - | 24 | V |
| V _{EB} | emitter-base voltage | open collector | - | - | 2 | V |
| l _C | collector current | | - | - | 200 | mA |
| Pi | input power | Z _S = 50 Ω | - | - | 20 | dBm |
| Tj | junction temperature | | -40 | - | +150 | °C |
| P _{tot} | total power dissipation | T _{sp} ≤ 90 °C | <u>[1]</u> _ | - | 2000 | mW |

[1] T_{sp} is the temperature at the solder point of the collector lead.

BFU590G Product data sheet

8. Thermal characteristics

| Table 8. | Thermal characteristics | | | |
|-----------------------|--|------------|-----|------|
| Symbol | Parameter | Conditions | Тур | Unit |
| R _{th(j-sp)} | thermal resistance from junction to solder point | <u>[1]</u> | 30 | K/W |

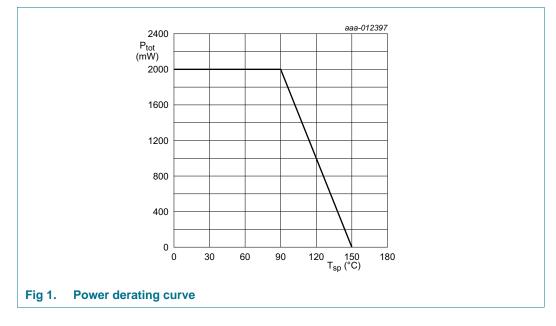
[1] T_{sp} is the temperature at the solder point of the collector lead.

 T_{sp} has the following relation to the ambient temperature T_{amb} :

 $T_{sp} = T_{amb} + P \times R_{th(sp-a)}$

With P being the power dissipation and $R_{th(sp-a)}$ being the thermal resistance between the solder point and ambient. $R_{th(sp-a)}$ is determined by the heat transfer properties in the application.

The heat transfer properties are set by the application board materials, the board layout and the environment e.g. housing.



9. Characteristics

Table 9. Characteristics

 $T_{amb} = 25$ °C unless otherwise specified

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|----------------------|-------------------------------------|--|-----|-----|-----|------|
| V _{(BR)CBO} | collector-base breakdown voltage | I _C = 100 nA; I _E = 0 mA | 24 | - | - | V |
| V _{(BR)CEO} | collector-emitter breakdown voltage | I _C = 150 nA; I _B = 0 mA | 12 | - | - | V |
| I _C | collector current | | - | 80 | 200 | mA |
| I _{CBO} | collector-base cut-off current | I _E = 0 mA; V _{CB} = 8 V | - | <1 | - | nA |
| h _{FE} | DC current gain | $I_{C} = 80 \text{ mA}; V_{CE} = 8 \text{ V}$ | 60 | 95 | 130 | |
| C _e | emitter capacitance | V _{EB} = 0.5 V; f = 1 MHz | - | 3.9 | - | pF |
| C _{re} | feedback capacitance | V _{CE} = 8 V; f = 1 MHz | - | 1.1 | - | pF |
| C _c | collector capacitance | V _{CB} = 8 V; f = 1 MHz | - | 1.9 | - | pF |
| f _T | transition frequency | $I_{C} = 50 \text{ mA}; V_{CE} = 8 \text{ V}; f = 900 \text{ MHz}$ | - | 8.5 | - | GHz |

NPN wideband silicon RF transistor

Table 9. Characteristics ...continued

 $T_{amb} = 25 \ ^{\circ}C$ unless otherwise specified

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|--------------------------------|---------------------------------------|--|-----|------|-----|------|
| G _{p(max)} | maximum power gain | f = 433 MHz; V _{CE} = 8 V | 1 | | | |
| | | I _C = 10 mA | - | 18.5 | - | dB |
| | | I _C = 50 mA | - | 19.5 | - | dB |
| | | I _C = 80 mA | - | 19.5 | - | dB |
| | | f = 900 MHz; V _{CE} = 8 V | 1 | | | |
| | | I _C = 10 mA | - | 13.5 | - | dB |
| | | I _C = 50 mA | - | 13 | - | dB |
| | | I _C = 80 mA | - | 13 | - | dB |
| | | f = 1800 MHz; V _{CE} = 8 V | 1 | | | |
| | | I _C = 10 mA | - | 8 | - | dB |
| | | I _C = 50 mA | - | 8 | - | dB |
| | | I _C = 80 mA | - | 8 | - | dB |
| s ₂₁ ² | insertion power gain | f = 433 MHz; V _{CE} = 8 V | | | | |
| | | I _C = 10 mA | - | 16 | - | dB |
| | | I _C = 50 mA | - | 17.5 | - | dB |
| | | I _C = 80 mA | - | 17.5 | - | dB |
| | | f = 900 MHz; V _{CE} = 8 V | | | | |
| | | I _C = 10 mA | - | 10 | - | dB |
| | | I _C = 50 mA | - | 11 | - | dB |
| | | I _C = 80 mA | - | 11 | - | dB |
| | | f = 1800 MHz; V _{CE} = 8 V | | | | |
| | | I _C = 10 mA | - | 4.5 | - | dB |
| | | I _C = 50 mA | - | 5.5 | - | dB |
| | | I _C = 80 mA | - | 5.5 | - | dB |
| P _{L(1dB)} | output power at 1 dB gain compression | f = 433 MHz; V_{CE} = 8 V; Z_{S} = Z_{L} = 50 Ω | | | | |
| | | I _C = 50 mA | - | 20 | - | dBm |
| | | I _C = 80 mA | - | 22.5 | - | dBm |
| | | f = 900 MHz; V_{CE} = 8 V; Z_{S} = Z_{L} = 50 Ω | | | | |
| | | I _C = 50 mA | - | 19.5 | - | dBm |
| | | I _C = 80 mA | - | 21.5 | - | dBm |
| | | f = 1800 MHz; V_{CE} = 8 V; Z_{S} = Z_{L} = 50 Ω | | | | |
| | | I _C = 50 mA | - | 18.5 | - | dBm |
| | | I _C = 80 mA | - | 21 | - | dBm |

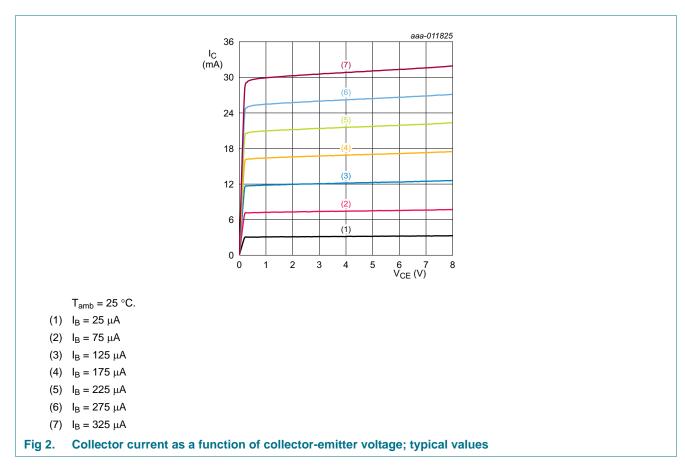
NPN wideband silicon RF transistor

Table 9. Characteristics ...continued

 $T_{amb} = 25 \ ^{\circ}C \ unless \ otherwise \ specified$

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|------------------|------------------------------------|---|-----|------|-----|------|
| IP3 _o | output third-order intercept point | $ f_1 = 433 \text{ MHz}; \ f_2 = 434 \text{ MHz}; \ V_{CE} = 8 \text{ V}; \\ Z_S = Z_L = 50 \ \Omega $ | | | | - |
| | | I _C = 50 mA | - | 29.5 | - | dBm |
| | | I _C = 80 mA | - | 32 | - | dBm |
| | | $\label{eq:f1} \begin{array}{l} f_1 = 900 \; MHz; f_2 = 901 \; MHz; V_{CE} = 8 \; V; \\ Z_S = Z_L = 50 \; \Omega \end{array}$ | | | | - |
| | | I _C = 50 mA | - | 29 | - | dBm |
| | | I _C = 80 mA | - | 31 | - | dBm |
| | | f_1 = 1800 MHz; f_2 = 1801 MHz; V _{CE} = 8 V; Z _S = Z _L = 50 Ω | | | | - |
| | | I _C = 50 mA | - | 28 | - | dBm |
| | | I _C = 80 mA | - | 30.5 | - | dBm |

 $\label{eq:general} \mbox{[1]} \quad \mbox{If } K > 1 \mbox{ then } G_{p(max)} \mbox{ is the maximum power gain. If } K < 1 \mbox{ then } G_{p(max)} = MSG.$

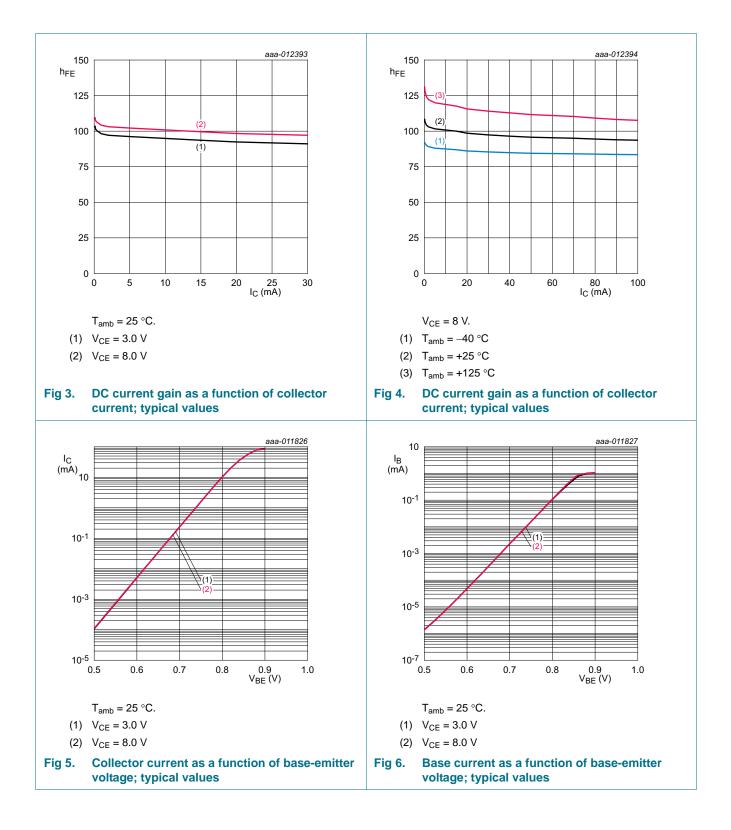


9.1 Graphs

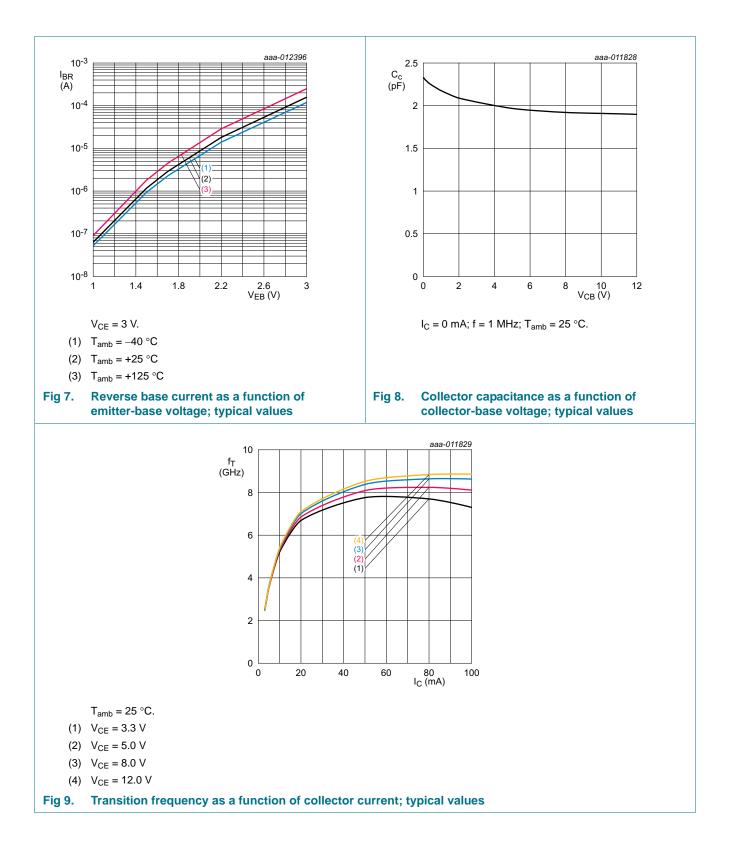
NXP Semiconductors

BFU590G

NPN wideband silicon RF transistor



NPN wideband silicon RF transistor



Product data sheet

BFU590G

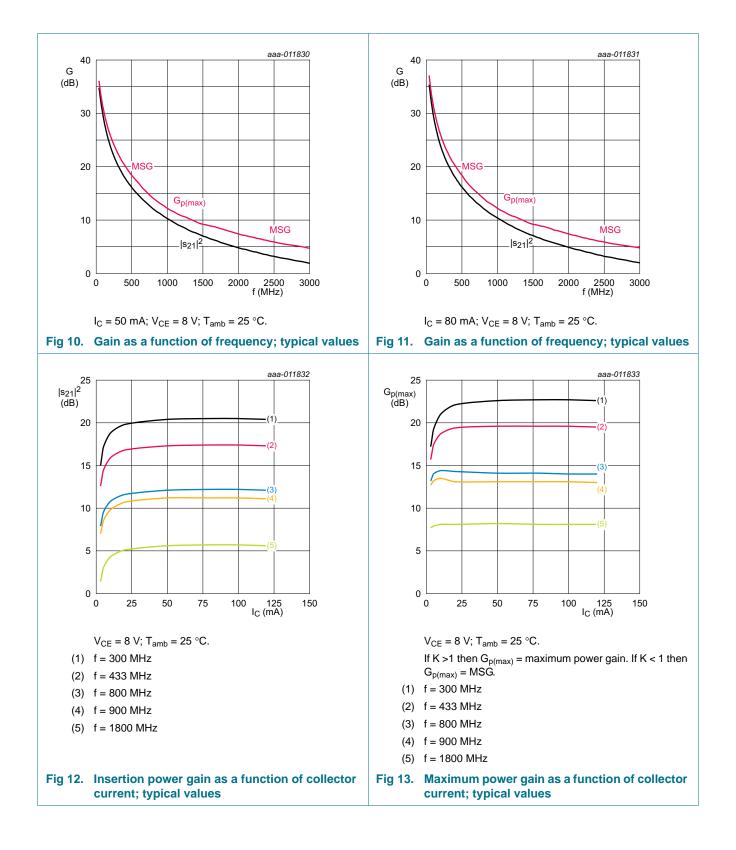
All information provided in this document is subject to legal disclaimers.

8 of 19

NXP Semiconductors

BFU590G

NPN wideband silicon RF transistor

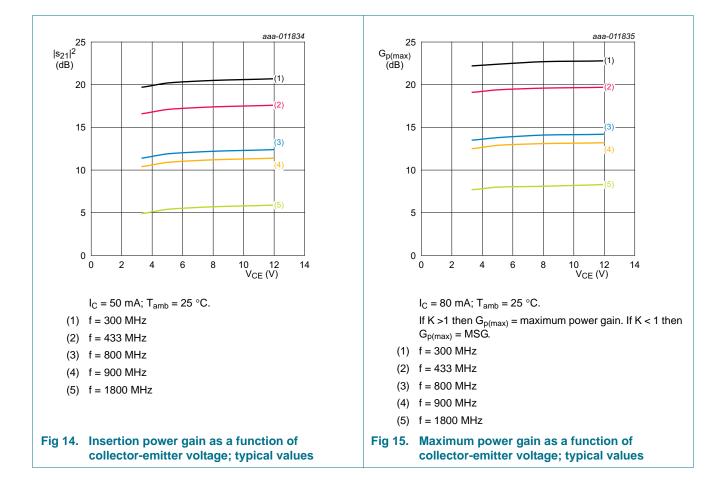


Product data sheet

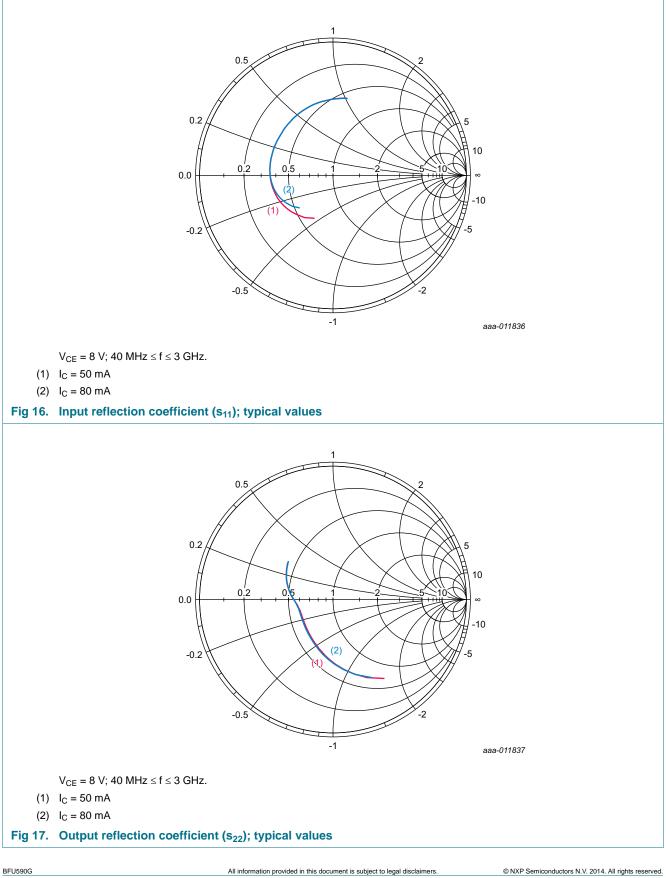
NXP Semiconductors

BFU590G

NPN wideband silicon RF transistor

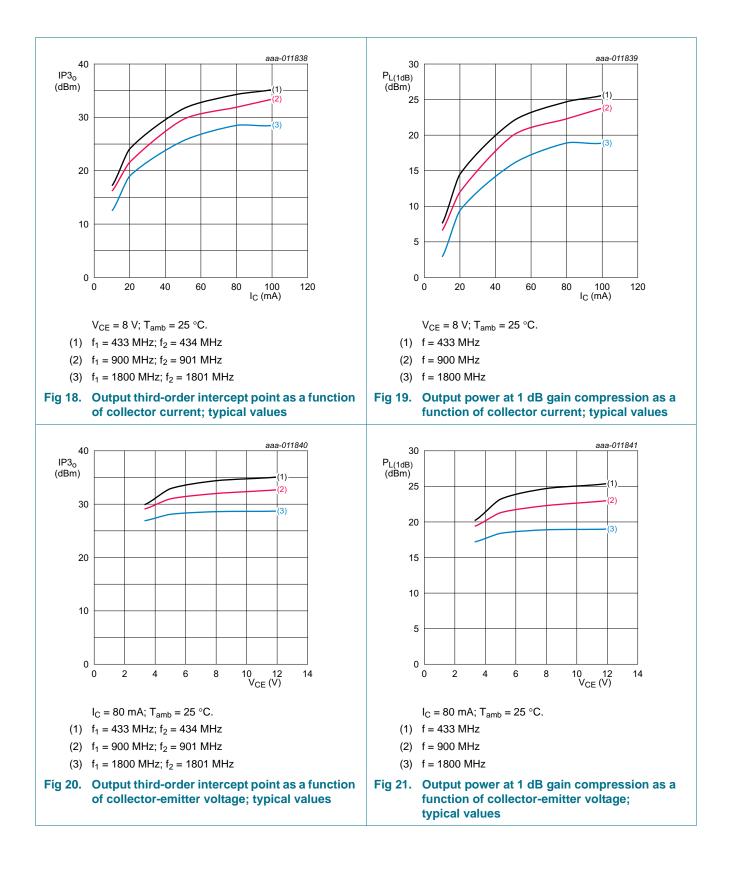


NPN wideband silicon RF transistor



Product data sheet

NPN wideband silicon RF transistor



10. Application information

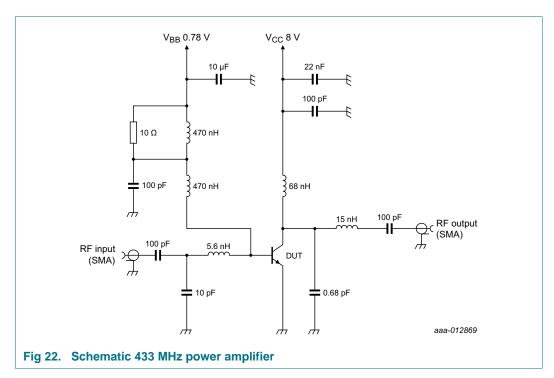
More information about the following application example can be found in the application notes. See <u>Section 5 "Design support</u>".

The following application example can be implemented using the evaluation kit. See <u>Section 3 "Ordering information"</u> for the order type number.

The following application example can be simulated using the simulation package. See <u>Section 5 "Design support"</u>.

10.1 Application example: 433 MHz PA

More detailed information of the application example can be found in the application note: *AN11503.*



Remark: fine tuning of components maybe required depending on PCB parasitics.

Table 10. Application performance data at 433 MHz

| $I_{\rm CC} = 100 \ m\text{A}; \ V_{\rm CC} = 8 \ V$ | | | | | | |
|--|---------------------------------------|------------|-----|-----|-----|------|
| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
| s ₂₁ ² | insertion power gain | | - | 15 | - | dB |
| S ₁₁ ² | input return loss | | - | -7 | - | dB |
| P _{L(1dB)} | output power at 1 dB gain compression | | - | 26 | - | dBm |
| ηc | collector efficiency | | - | 60 | - | % |

Product data sheet

10.2 Application example: 866 MHz PA

More detailed information of the application example can be found in the application note: AN11501.

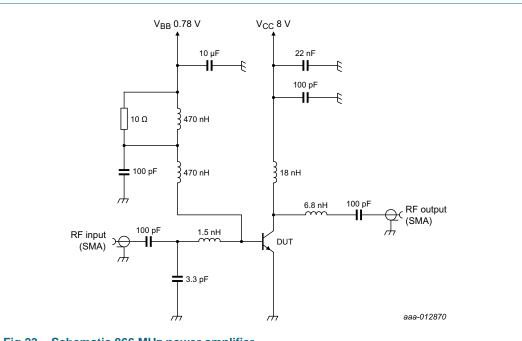


Fig 23. Schematic 866 MHz power amplifier

Remark: fine tuning of components maybe required depending on PCB parasitics.

Table 11. Application performance data at 866 MHz $l_{cc} = 100 \text{ mA}$; $V_{cc} = 8 \text{ V}$

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|--------------------------------|---------------------------------------|------------|-----|-----|-----|------|
| $ s_{21} ^2$ | insertion power gain | | - | 10 | - | dB |
| S ₁₁ ² | input return loss | | - | -12 | - | dB |
| P _{L(1dB)} | output power at 1 dB gain compression | | - | 27 | - | dBm |
| ης | collector efficiency | | - | 55 | - | % |

BFU590G **Product data sheet**

NPN wideband silicon RF transistor

11. Package outline

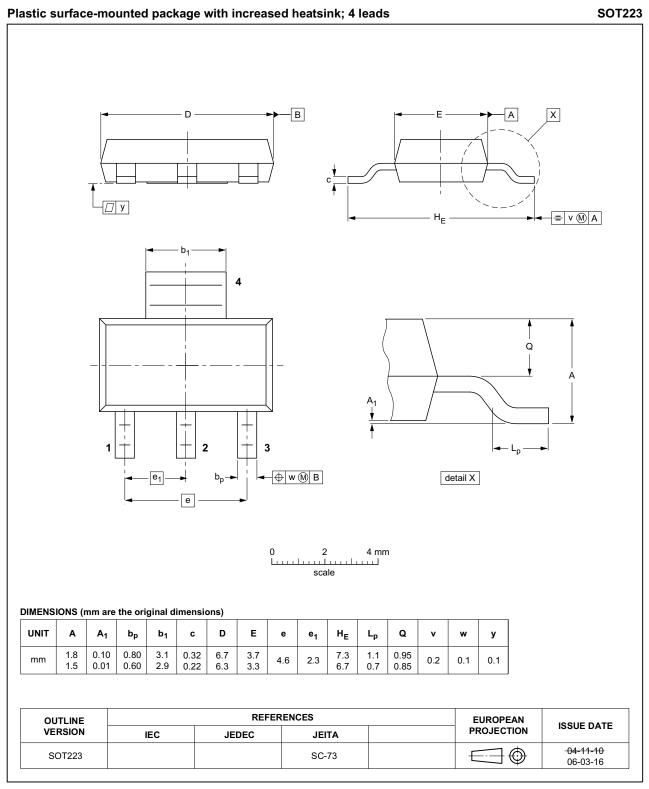


Fig 24. Package outline SOT223

All information provided in this document is subject to legal disclaimers.

12. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

13. Abbreviations

| Acronym | Description | | | |
|---------|------------------------------------|--|--|--|
| AEC | Automotive Electronics Council | | | |
| ISM | Industrial, Scientific and Medical | | | |
| LNA | Low-Noise Amplifier | | | |
| MSG | Maximum Stable Gain | | | |
| NPN | Negative-Positive-Negative | | | |
| PA | Power Amplifier | | | |
| SMA | SubMiniature version A | | | |

14. Revision history

Table 13. Revision history

| Document ID | Release date | Data sheet status | Change notice | Supersedes |
|-------------|--------------|--------------------|---------------|------------|
| BFU590G v.1 | 20140428 | Product data sheet | - | - |

15. Legal information

15.1 Data sheet status

| Document status[1][2] | Product status ^[3] | Definition |
|--------------------------------|-------------------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

15.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

15.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use in automotive applications - This NXP

Semiconductors product has been qualified for use in automotive applications. Unless otherwise agreed in writing, the product is not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nxp.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

NPN wideband silicon RF transistor

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

16. Contact information

For more information, please visit: http://www.nxp.com

For sales office addresses, please send an email to: salesaddresses@nxp.com

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

15.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

18 of 19

NPN wideband silicon RF transistor

17. Contents

| 1 | Product profile 1 |
|------|------------------------------------|
| 1.1 | General description 1 |
| 1.2 | Features and benefits 1 |
| 1.3 | Applications 1 |
| 1.4 | Quick reference data 1 |
| 2 | Pinning information 2 |
| 3 | Ordering information 2 |
| 4 | Marking 2 |
| 5 | Design support 3 |
| 6 | Limiting values 3 |
| 7 | Recommended operating conditions 3 |
| 8 | Thermal characteristics 4 |
| 9 | Characteristics 4 |
| 9.1 | Graphs 6 |
| 10 | Application information 13 |
| 10.1 | Application example: 433 MHz PA 13 |
| 10.2 | Application example: 866 MHz PA 14 |
| 11 | Package outline 15 |
| 12 | Handling information 16 |
| 13 | Abbreviations |
| 14 | Revision history 16 |
| 15 | Legal information 17 |
| 15.1 | Data sheet status 17 |
| 15.2 | Definitions 17 |
| 15.3 | Disclaimers |
| 15.4 | Trademarks 18 |
| 16 | Contact information 18 |
| 17 | Contents 19 |

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

© NXP Semiconductors N.V. 2014.

All rights reserved.

For more information, please visit: http://www.nxp.com For sales office addresses, please send an email to: salesaddresses@nxp.com

Date of release: 28 April 2014 Document identifier: BFU590G