Embedded Artists
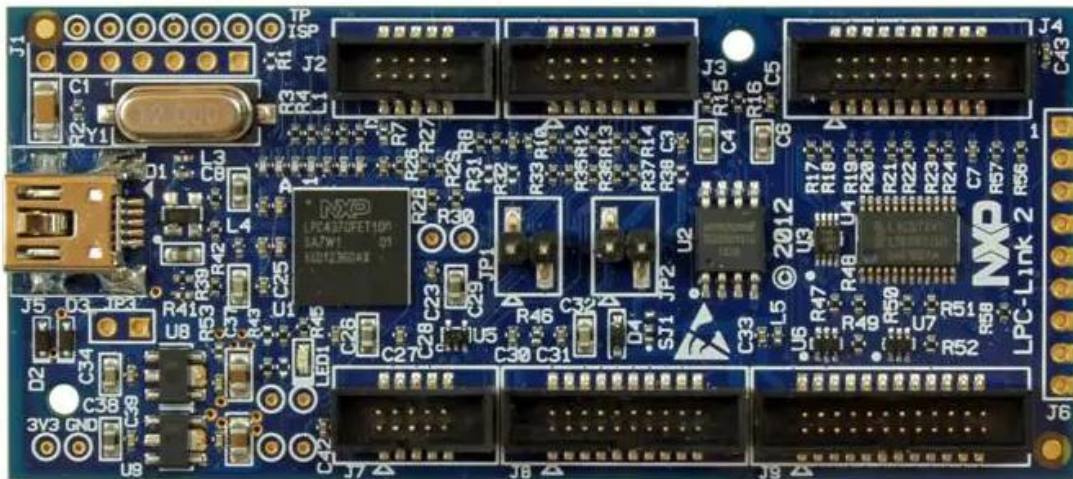
LPC-Link2

Overview

Jointly developed by NXP and Embedded Artists, the LPC-Link 2 is an extensible, stand-alone debug probe that can be configured to support various development tools and IDEs using a variety of different downloadable firmware images. It can also be used as an evaluation board in its own right for the NXP® LPC4370 triple core MCU.

Available firmware images include:

- J-Link On-Board by Segger
- CMSIS-DAP
  - Stand-alone debug adapter
  - CMSIS-DAP by Arm
  - J-Link by Segger

LPC-Link2 is an extensible, stand-alone debug adapter that can be configured to support various development tools and IDEs by downloadable firmwares.

The board is supported by the MCUXpresso IDE and all development environments that supports the J-Link and CMSIS-DAP interface.

This product is delivered with a <u>10-pos IDC Ribbon Cable</u>. Note that an <u>adapter</u> is needed to connect to the older 2×10-pos 100 mil pitch ARM debug connectors.

Specifications

Technical and Functional Specifications

When used as an LPC4370 development evaluation board	LPC4370 Arm Cortex-M4/dual Cortex-M0 processor in a BGA100 package, with all cores running at up to 204 MHz
	High-speed USB port
	8Mb quad SPI flash
	Single jumper DFU/flash boot mode selection
	Analog, digital, and serial expansion headers
	Standard 10-pin Arm debug probe connector
	ETM trace connector
When used as a debug probe	High-speed USB port
	Compatible with LPCXpresso IDE from NXP using CMSIS-DAP firmware image
	Compatible with MPCXpresso IDE (available March 2017) using CMSIS-DAP or J-Link firmware images*
	Compatible with tools/IDEs that support the SEGGER J-Link and/or CMSIS-DAP protocols via downloadable firmware image
	*When using J-Link firmware image on MCUXpresso IDE trace and power measurement features are not available.