

# LP2989

*LP2989 Micropower/Low Noise, 500 mA Ultra Low-Dropout Regulator For Use  
with Ceramic Output Capacitors*



Literature Number: SNVS083M

# LP2989

## Micropower/Low Noise, 500 mA Ultra Low-Dropout Regulator

### For Use with Ceramic Output Capacitors

#### General Description

The LP2989 is a fixed-output 500 mA precision LDO regulator designed for use with ceramic output capacitors.

Output noise can be reduced to 18 $\mu$ V (typical) by connecting an external 10 nF capacitor to the bypass pin.

Using an optimized VIP™ (Vertically Integrated PNP) process, the LP2989 delivers superior performance:

**Dropout Voltage:** Typically 310 mV @ 500 mA load, and 1 mV @ 100  $\mu$ A load.

**Ground Pin Current:** Typically 3 mA @ 500 mA load, and 110  $\mu$ A @ 100  $\mu$ A load.

**Sleep Mode:** The LP2989 draws less than 0.8  $\mu$ A quiescent current when shutdown pin is pulled low.

**Error Flag:** The built-in error flag goes low when the output drops approximately 5% below nominal.

**Precision Output:** Guaranteed output voltage accuracy is 0.75% ("A" grade) and 1.25% (standard grade) at room temperature.

For output voltages < 2V, see LP2989LV datasheet.

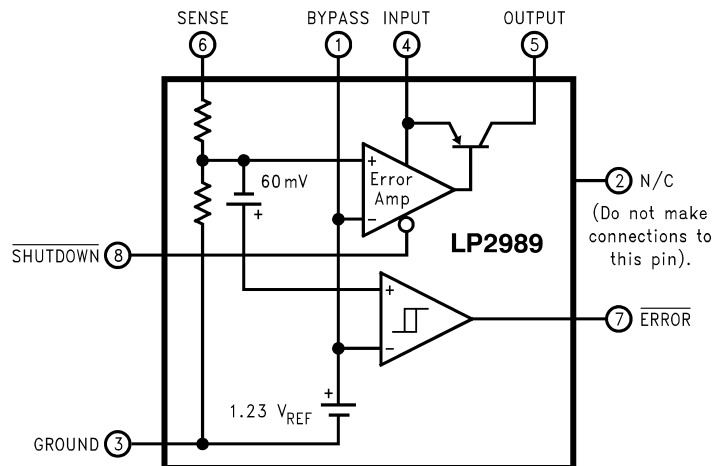
#### Features

- Ultra low dropout voltage
- Guaranteed 500 mA continuous output current
- Very low output noise with external capacitor
- SO-8, Mini SO-8, 8 Lead LLP surface mount packages
- <0.8  $\mu$ A quiescent current when shutdown
- Low ground pin current at all loads
- 0.75% output voltage accuracy ("A" grade)
- High peak current capability (800 mA typical)
- Wide supply voltage range (16V max)
- Overtemperature/overcurrent protection
- -40°C to +125°C junction temperature range

#### Applications

- Notebook/Desktop PC
- PDA/Palmtop Computer
- Wireless Communication Terminals
- SMPS Post-Regulator

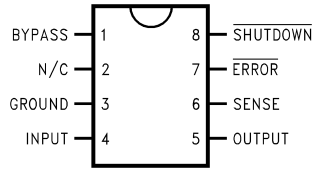
#### Block Diagram



10133901

## Connection Diagrams

### Surface Mount Packages:

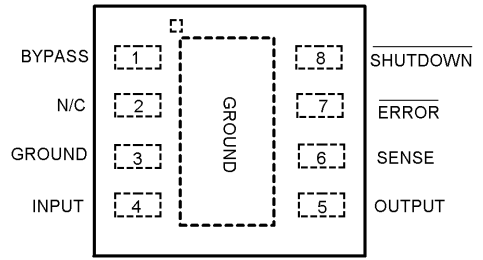


10133902

### SO-8/Mini SO-8 Package

See NS Package Drawing Numbers M08A/MUA08A

### 8 Lead LLP Surface Mount Package

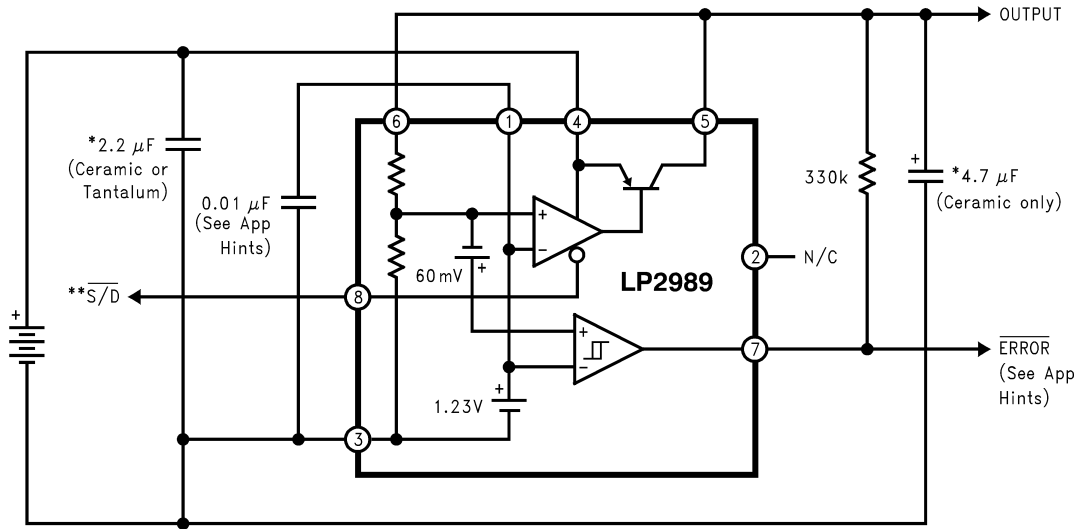


10133950

### Top View

See NS Package Number LDC08A

## Basic Application Circuit



10133903

\*Capacitance values shown are minimum required to assure stability, but may be increased without limit. Larger output capacitor provides improved dynamic response. See Application Hints.

\*\*Shutdown must be actively terminated (see App. Hints). Tie to INPUT (Pin4) if not used

## Ordering Information

TABLE 1. Package Marking and Ordering Information

Output Voltage	Grade	Order Information	Package Marking	Supplied as:
<b>8 Lead LLP</b>				
2.5	A	LP2989AILD-2.5	L01FA	1000 Units on Tape and Reel
2.5	A	LP2989AILD-2.5	L01FA	4500 Units on Tape and Reel
2.5	STD	LP2989ILD-2.5	L01FAB	1000 Units on Tape and Reel
2.5	STD	LP2989ILD-2.5	L01FAB	4500 Units on Tape and Reel
2.8	A	LP2989AILD-2.8	L000A	1000 Units on Tape and Reel
2.8	A	LP2989AILD-2.8	L000A	4500 Units on Tape and Reel
2.8	STD	LP2989ILD-2.8	L000AB	1000 Units on Tape and Reel
2.8	STD	LP2989ILD-2.8	L000AB	4500 Units on Tape and Reel
2.85	A	LP2989AILD-285	L01TA	1000 Units on Tape and Reel
2.85	A	LP2989AILD-285	L01TA	4500 Units on Tape and Reel
2.85	STD	LP2989ILD-285	L01TAB	1000 Units on Tape and Reel
2.85	STD	LP2989ILD-285	L01TAB	4500 Units on Tape and Reel
3.0	A	LP2989AILD-3.0	L01HA	1000 Units on Tape and Reel
3.0	A	LP2989AILD-3.0	L01HA	4500 Units on Tape and Reel
3.0	STD	LP2989ILD-3.0	L01HAB	1000 Units on Tape and Reel
3.0	STD	LP2989ILD-3.0	L01HAB	4500 Units on Tape and Reel
3.3	A	LP2989AILD-3.3	L01JA	1000 Units on Tape and Reel
3.3	A	LP2989AILD-3.3	L01JA	4500 Units on Tape and Reel
3.3	STD	LP2989ILD-3.3	L01JAB	1000 Units on Tape and Reel
3.3	STD	LP2989ILD-3.3	L01JAB	4500 Units on Tape and Reel
3.6	A	LP2989AILD-3.6	L019A	1000 Units on Tape and Reel
3.6	A	LP2989AILD-3.6	L019A	4500 Units on Tape and Reel
3.6	STD	LP2989ILD-3.6	L019AB	1000 Units on Tape and Reel
3.6	STD	LP2989ILD-3.6	L019AB	4500 Units on Tape and Reel
4.0	A	LP2989AILD-4.0	L01LA	1000 Units on Tape and Reel
4.0	A	LP2989AILD-4.0	L01LA	4500 Units on Tape and Reel
4.0	STD	LP2989ILD-4.0	L01LAB	1000 Units on Tape and Reel
4.0	STD	LP2989ILD-4.0	L01LAB	4500 Units on Tape and Reel
5.0	A	LP2989AILD-5.0	L01KA	1000 Units on Tape and Reel
5.0	A	LP2989AILD-5.0	L01KA	4500 Units on Tape and Reel
5.0	STD	LP2989ILD-5.0	L01KAB	1000 Units on Tape and Reel
5.0	STD	LP2989ILD-5.0	L01KAB	4500 Units on Tape and Reel
<b>8 Lead MSOP (MM)</b>				
2.5	A	LP2989A IMM-2.5	LA0A	1000 Units on Tape and Reel
2.5	A	LP2989A IMM-2.5	LA0A	3500 Units on Tape and Reel
2.5	STD	LP2989 IMM-2.5	LA0B	1000 Units on Tape and Reel
2.5	STD	LP2989 IMM-2.5	LA0B	3500 Units on Tape and Reel
2.8	A	LP2989A IMM-2.8	LA6A	1000 Units on Tape and Reel
2.8	A	LP2989A IMM-2.8	LA6A	3500 Units on Tape and Reel
2.8	STD	LP2989 IMM-2.8	LA6B	1000 Units on Tape and Reel
2.8	STD	LP2989 IMM-2.8	LA6B	3500 Units on Tape and Reel
3.0	A	LP2989A IMM-3.0	LA1A	1000 Units on Tape and Reel
3.0	A	LP2989A IMM-3.0	LA1A	3500 Units on Tape and Reel
3.0	STD	LP2989 IMM-3.0	LA1B	1000 Units on Tape and Reel
3.0	STD	LP2989 IMM-3.0	LA1B	3500 Units on Tape and Reel
3.3	A	LP2989A IMM-3.3	LA2A	1000 Units on Tape and Reel
3.3	A	LP2989A IMM-3.3	LA2A	3500 Units on Tape and Reel

## Ordering Information (Continued)

**TABLE 1. Package Marking and Ordering Information (Continued)**

Output Voltage	Grade	Order Information	Package Marking	Supplied as:
3.3	STD	LP2989IMM-3.3	LA2B	1000 Units on Tape and Reel
3.3	STD	LP2989IMMX-3.3	LA2B	3500 Units on Tape and Reel
5.0	A	LP2989AIMM-5.0	LA4A	1000 Units on Tape and Reel
5.0	A	LP2989AIMMX-5.0	LA4A	3500 Units on Tape and Reel
5.0	STD	LP2989IMM-5.0	LA4B	1000 Units on Tape and Reel
5.0	STD	LP2989IMMX-5.0	LA4B	3500 Units on Tape and Reel
<b>SO-8 (M)</b>				
2.5	A	LP2989AIMX-2.5	LP2989AIM2.5	2500 Units on Tape and Reel
2.5	A	LP2989AIM-2.5	LP2989AIM2.5	Shipped in Anti-Static Rails
2.5	STD	LP2989IMX-2.5	LP2989IM2.5	2500 Units on Tape and Reel
2.5	STD	LP2989IM-2.5	LP2989IM2.5	Shipped in Anti-Static Rails
3.0	A	LP2989AIMX-3.0	LP2989AIM3.0	2500 Units on Tape and Reel
3.0	A	LP2989AIM-3.0	LP2989AIM3.0	Shipped in Anti-Static Rails
3.0	STD	LP2989IMX-3.0	LP2989IM3.0	2500 Units on Tape and Reel
3.0	STD	LP2989IM-3.0	LP2989IM3.0	Shipped in Anti-Static Rails
3.3	A	LP2989AIMX-3.3	LP2989AIM3.3	2500 Units on Tape and Reel
3.3	A	LP2989AIM-3.3	LP2989AIM3.3	Shipped in Anti-Static Rails
3.3	STD	LP2989IMX-3.3	LP2989IM3.3	2500 Units on Tape and Reel
3.3	STD	LP2989IM-3.3	LP2989IM3.3	Shipped in Anti-Static Rails
5.0	A	LP2989AIMX-5.0	LP2989AIM5.0	2500 Units on Tape and Reel
5.0	A	LP2989AIM-5.0	LP2989AIM5.0	Shipped in Anti-Static Rails
5.0	STD	LP2989IMX-5.0	LP2989IM5.0	2500 Units on Tape and Reel
5.0	STD	LP2989IM-5.0	LP2989IM5.0	Shipped in Anti-Static Rails

For output voltages < 2V, see LP2989LV datasheet.

### Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Storage Temperature Range	-65°C to +150°C
Operating Junction Temperature Range	-40°C to +125°C
Lead Temperature (Soldering, 5 seconds)	260°C
ESD Rating (Note 2)	2 kV
Power Dissipation (Note 3)	Internally Limited

Input Supply Voltage (Survival)	-0.3V to +16V
Input Supply Voltage (Operating)	2.1V to +16V
Sense Pin	-0.3V to +6V
Output Voltage (Survival) (Note 4)	-0.3V to +16V
I <sub>OUT</sub> (Survival)	Short Circuit Protected
Input-Output Voltage (Survival) (Note 5)	-0.3V to +16V

### Electrical Characteristics

Limits in standard typeface are for T<sub>J</sub> = 25°C, and limits in **boldface type** apply over the full operating temperature range. Unless otherwise specified: V<sub>IN</sub> = V<sub>O(NOM)</sub> + 1V, I<sub>L</sub> = 1 mA, C<sub>OUT</sub> = 4.7 μF, C<sub>IN</sub> = 2.2 μF, V<sub>S/D</sub> = 2V.

Symbol	Parameter	Conditions	Typical	LP2989AI-X.X (Note 6)		LP2989I-X.X (Note 6)		Units
				Min	Max	Min	Max	
V <sub>O</sub>	Output Voltage Tolerance	1 mA < I <sub>L</sub> < 500 mA V <sub>O(NOM)</sub> + 1V ≤ V <sub>IN</sub> ≤ 16V		-0.75	0.75	-1.25	1.25	%V <sub>NOM</sub>
				-1.5	1.5	-2.5	2.5	
				<b>-4.0</b>	<b>2.5</b>	<b>-5.0</b>	<b>3.5</b>	
		1 mA < I <sub>L</sub> < 500 mA V <sub>O(NOM)</sub> + 1V ≤ V <sub>IN</sub> ≤ 16V -25°C ≤ T <sub>J</sub> ≤ 125°C		<b>-3.5</b>	<b>2.5</b>	<b>-4.5</b>	<b>3.5</b>	
$\frac{\Delta V_O}{\Delta V_{IN}}$	Output Voltage Line Regulation	V <sub>O(NOM)</sub> + 1V ≤ V <sub>IN</sub> ≤ 16V	0.005		0.014		0.014	%V
					<b>0.032</b>		<b>0.032</b>	
$\frac{\Delta V_O}{\Delta I_L}$	Load Regulation	1 mA < I <sub>L</sub> < 500 mA	0.4					%V <sub>NOM</sub>
V <sub>IN</sub> -V <sub>O</sub>	Dropout Voltage (Note 7)	I <sub>L</sub> = 100 μA	1		3		3	mV
					<b>4</b>		<b>4</b>	
		I <sub>L</sub> = 200 mA	150		200		200	
					<b>300</b>		<b>300</b>	
		I <sub>L</sub> = 500 mA	310		425		425	
					<b>650</b>		<b>650</b>	
I <sub>GND</sub>	Ground Pin Current	I <sub>L</sub> = 100 μA	110		175		175	μA
					<b>200</b>		<b>200</b>	
		I <sub>L</sub> = 200 mA	1		2		2	mA
					<b>3.5</b>		<b>3.5</b>	
		I <sub>L</sub> = 500 mA	3		6		6	
					<b>9</b>		<b>9</b>	
		V <sub>S/D</sub> < 0.18V	<b>0.5</b>		<b>2</b>		<b>2</b>	μA
		V <sub>S/D</sub> < 0.4V	0.05		0.8		0.8	
I <sub>O(PK)</sub>	Peak Output Current	V <sub>OUT</sub> ≥ V <sub>O(NOM)</sub> - 5%	800	600		600		mA
I <sub>O(MAX)</sub>	Short Circuit Current	R <sub>L</sub> = 0 (Steady State) (Note 9)	1000					
e <sub>n</sub>	Output Noise Voltage (RMS)	BW = 100 Hz to 100 kHz, C <sub>OUT</sub> = 10 μF C <sub>BYPASS</sub> = .01 μF V <sub>OUT</sub> = 2.5V	18					μV(RMS)

## Electrical Characteristics (Continued)

Limits in standard typeface are for  $T_J = 25^\circ\text{C}$ , and limits in **boldface type** apply over the full operating temperature range. Unless otherwise specified:  $V_{IN} = V_O(\text{NOM}) + 1\text{V}$ ,  $I_L = 1\text{ mA}$ ,  $C_{OUT} = 4.7\ \mu\text{F}$ ,  $C_{IN} = 2.2\ \mu\text{F}$ ,  $V_{S/D} = 2\text{V}$ .

Symbol	Parameter	Conditions	Typical	LP2989AI-X.X (Note 6)		LP2989I-X.X (Note 6)		Units
				Min	Max	Min	Max	
$\frac{\Delta V_{OUT}}{\Delta V_{IN}}$	Ripple Rejection	$f = 1\text{ kHz}$ , $C_{OUT} = 10\ \mu\text{F}$	60					dB
$\frac{\Delta V_{OUT}}{\Delta T}$	Output Voltage Temperature Coefficient	(Note 8)	<b>20</b>					ppm/ $^\circ\text{C}$
<b>SHUTDOWN INPUT</b>								
$V_{S/D}$	S/D Input Voltage	$V_H = \text{O/P ON}$	1.4	<b>1.6</b>		<b>1.6</b>		V
		$V_L = \text{O/P OFF}$ $I_{IN} \leq 2\ \mu\text{A}$	0.50		<b>0.18</b>		<b>0.18</b>	
$I_{S/D}$	S/D Input Current	$V_{S/D} = 0$	0.001		<b>-1</b>		<b>-1</b>	$\mu\text{A}$
		$V_{S/D} = 5\text{V}$	5		<b>15</b>		<b>15</b>	
<b>ERROR COMPARATOR</b>								
$I_{OH}$	Output "HIGH" Leakage	$V_{OH} = 16\text{V}$	0.001		1		1	$\mu\text{A}$
$V_{OL}$	Output "LOW" Voltage	$V_{IN} = V_O(\text{NOM}) - 0.5\text{V}$ , $I_O(\text{COMP}) = 150\ \mu\text{A}$	150		220		220	mV
					<b>350</b>		<b>350</b>	
$V_{THR}(\text{MAX})$	Upper Threshold Voltage		-4.8	-6.0	-3.5	-6.0	-3.5	% $V_{OUT}$
$V_{THR}(\text{MIN})$	Lower Threshold Voltage		-6.6	-8.9	-4.9	-8.9	-4.9	
				<b>-13.0</b>	<b>-3.0</b>	<b>-13.0</b>	<b>-3.0</b>	
HYST	Hysteresis		2.0					

**Note 1:** Absolute Maximum Ratings indicate limits beyond which damage to the component may occur. Electrical specifications do not apply when operating the device outside of its rated operating conditions.

**Note 2:** ESD testing was performed using Human Body Model, a 100 pF capacitor discharged through a 1.5 k $\Omega$  resistor.

**Note 3:** The maximum allowable power dissipation is a function of the maximum junction temperature,  $T_J(\text{MAX})$ , the junction-to-ambient thermal resistance,  $\theta_{J-A}$ , and the ambient temperature,  $T_A$ . The maximum allowable power dissipation at any ambient temperature is calculated using:

$$P(\text{MAX}) = \frac{T_J(\text{MAX}) - T_A}{\theta_{J-A}}$$

The value of  $\theta_{J-A}$  for the SO-8 (M) package is 160 $^\circ\text{C}/\text{W}$  and the mini SO-8 (MM) package is 200 $^\circ\text{C}/\text{W}$ . The value  $\theta_{J-A}$  for the LLP (LD) package is specifically dependent on PCB trace area, trace material, and the number of layers and thermal vias. For improved thermal resistance and power dissipation for the LLP package, refer to Application Note AN-1187. Exceeding the maximum allowable power dissipation will cause excessive die temperature, and the regulator will go into thermal shutdown.

**Note 4:** If used in a dual-supply system where the regulator load is returned to a negative supply, the LP2989 output must be diode-clamped to ground.

**Note 5:** The output PNP structure contains a diode between the  $V_{IN}$  and  $V_{OUT}$  terminals that is normally reverse-biased. Forcing the output above the input will turn on this diode and may induce a latch-up mode which can damage the part (see Application Hints).

**Note 6:** Limits are 100% production tested at 25 $^\circ\text{C}$ . Limits over the operating temperature range are guaranteed through correlation using Statistical Quality Control (SQC) methods. The limits are used to calculate National's Average Outgoing Quality Level (AOQL).

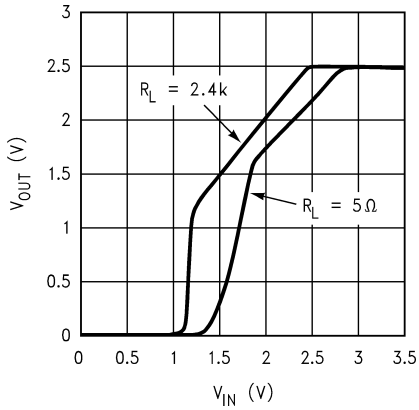
**Note 7:** Dropout voltage is defined as the input to output differential at which the output voltage drops 100 mV below the value measured with a 1V differential.

**Note 8:** Temperature coefficient is defined as the maximum (worst-case) change divided by the total temperature range.

**Note 9:** See Typical Performance Characteristics curves.

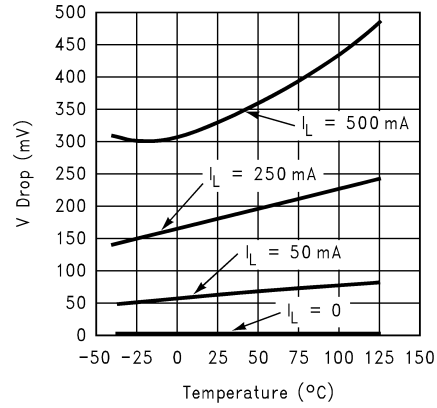
**Typical Performance Characteristics** Unless otherwise specified:  $T_A = 25^\circ\text{C}$ ,  $C_{OUT} = 4.7\ \mu\text{F}$ ,  $C_{IN} = 2.2\ \mu\text{F}$ , S/D is tied to  $V_{IN}$ ,  $V_{IN} = V_O(\text{NOM}) + 1\text{V}$ ,  $I_L = 1\ \text{mA}$ ,  $V_{OUT} = 2.5\text{V}$ .

**Dropout Characteristics**



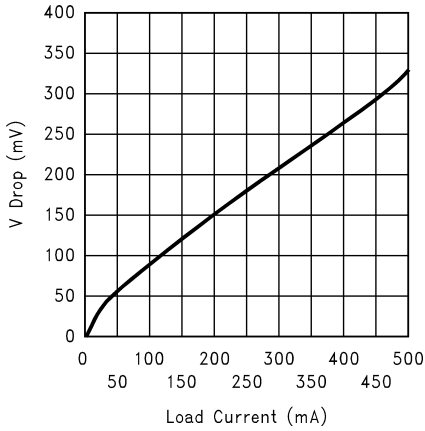
10133921

**Dropout Voltage vs Temperature**



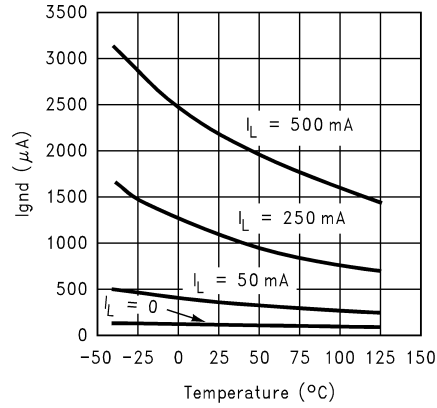
10133920

**Dropout Voltage vs Load Current**



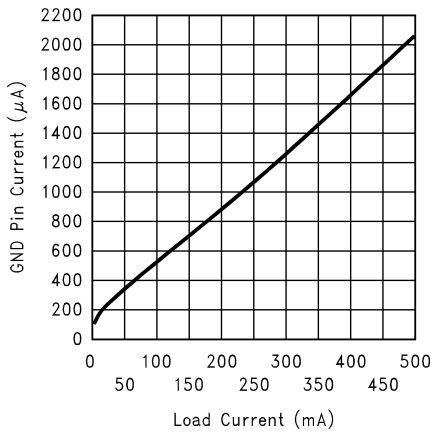
10133919

**GND Pin Current vs Temperature and Load**



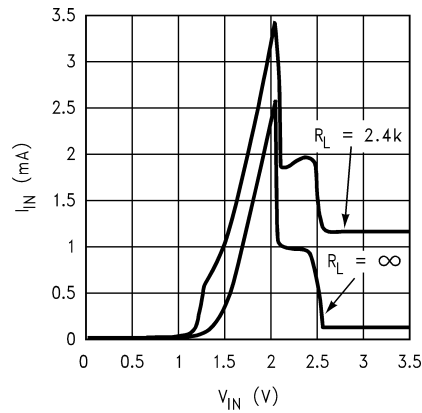
10133923

**Ground Pin Current vs Load Current**



10133922

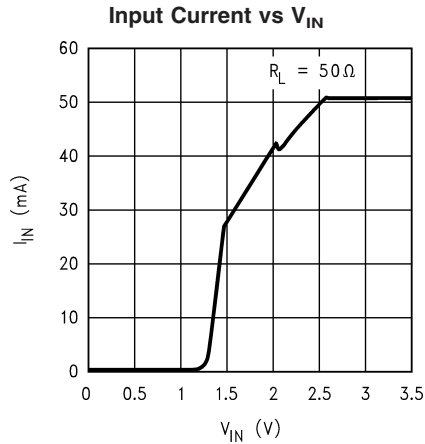
**Input Current vs  $V_{IN}$**



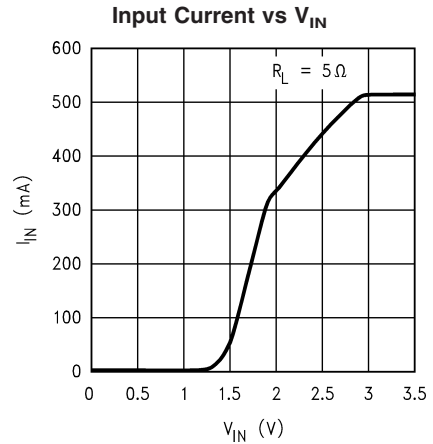
10133925



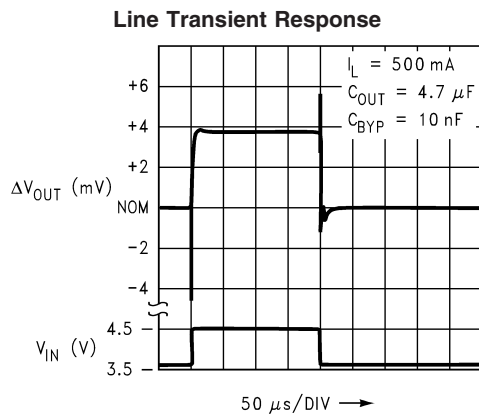
**Typical Performance Characteristics** Unless otherwise specified:  $T_A = 25^\circ\text{C}$ ,  $C_{OUT} = 4.7 \mu\text{F}$ ,  $C_{IN} = 2.2 \mu\text{F}$ , S/D is tied to  $V_{IN}$ ,  $V_{IN} = V_{O(NOM)} + 1\text{V}$ ,  $I_L = 1 \text{ mA}$ ,  $V_{OUT} = 2.5\text{V}$ . (Continued)



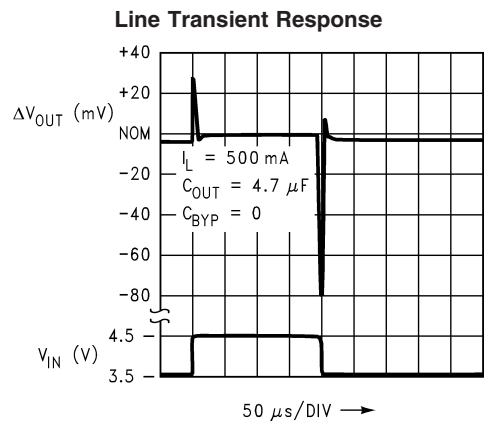
10133905



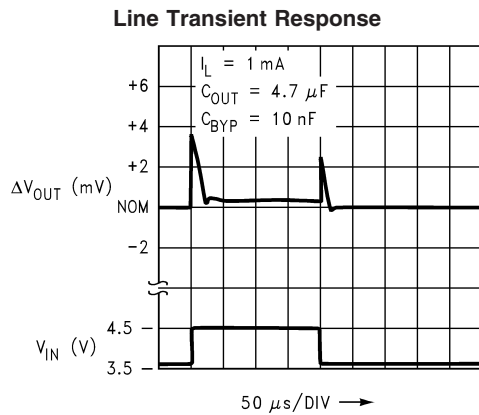
10133906



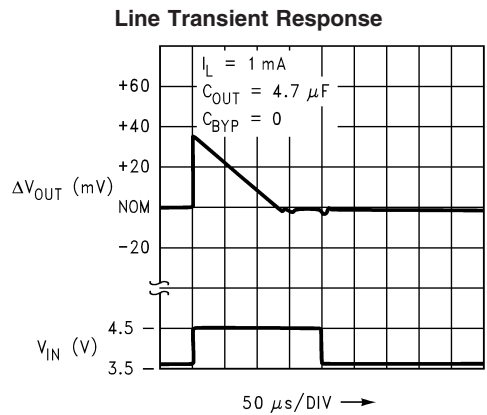
10133915



10133916



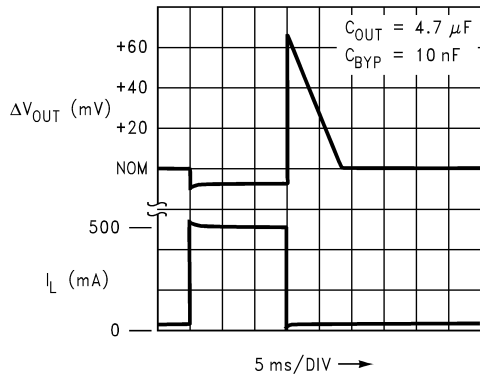
10133911



10133926

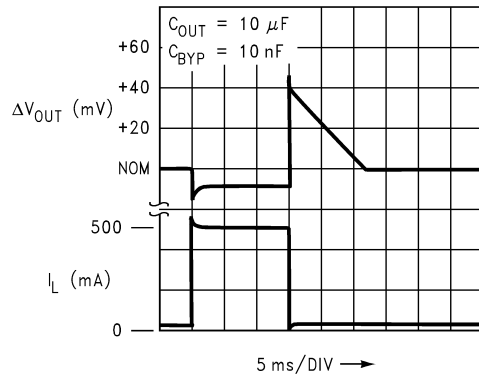
**Typical Performance Characteristics** Unless otherwise specified:  $T_A = 25^\circ\text{C}$ ,  $C_{OUT} = 4.7 \mu\text{F}$ ,  $C_{IN} = 2.2 \mu\text{F}$ , S/D is tied to  $V_{IN}$ ,  $V_{IN} = V_{O(NOM)} + 1\text{V}$ ,  $I_L = 1 \text{mA}$ ,  $V_{OUT} = 2.5\text{V}$ . (Continued)

**Load Transient Response**



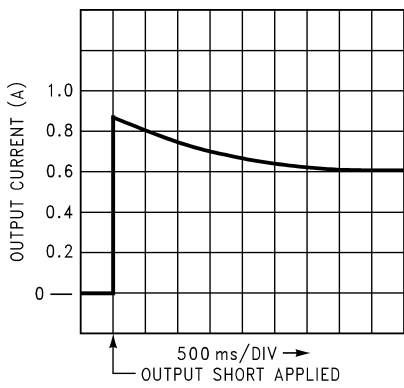
10133917

**Load Transient Response**



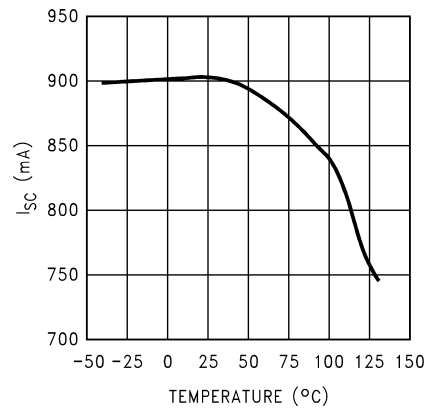
10133918

**Short Circuit Current**



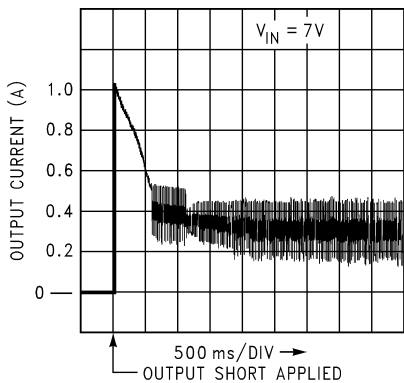
10133934

**Short Circuit Current vs Temperature**



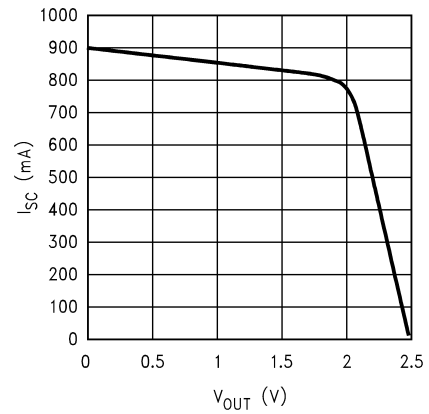
10133932

**Short Circuit Current**



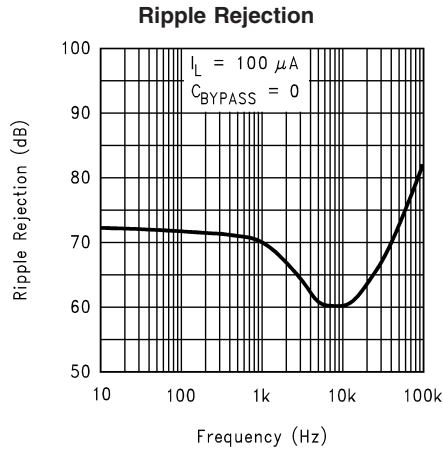
10133935

**Short Circuit Current vs  $V_{OUT}$**

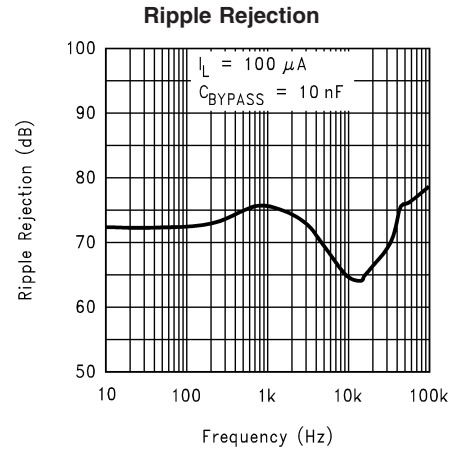


10133933

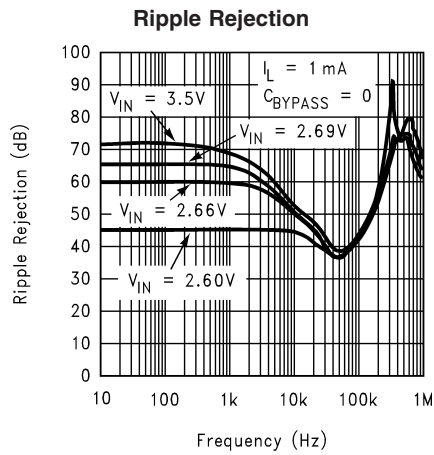
**Typical Performance Characteristics** Unless otherwise specified:  $T_A = 25^\circ\text{C}$ ,  $C_{OUT} = 4.7 \mu\text{F}$ ,  $C_{IN} = 2.2 \mu\text{F}$ , S/D is tied to  $V_{IN}$ ,  $V_{IN} = V_{O(NOM)} + 1\text{V}$ ,  $I_L = 1 \text{mA}$ ,  $V_{OUT} = 2.5\text{V}$ . (Continued)



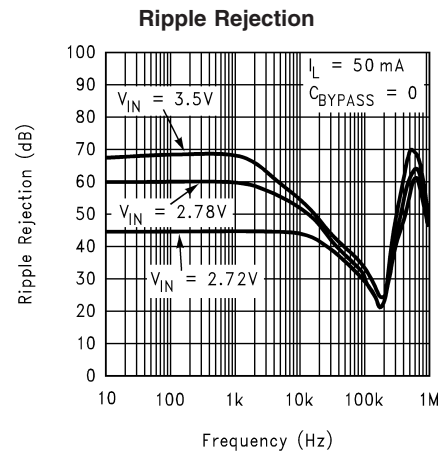
10133939



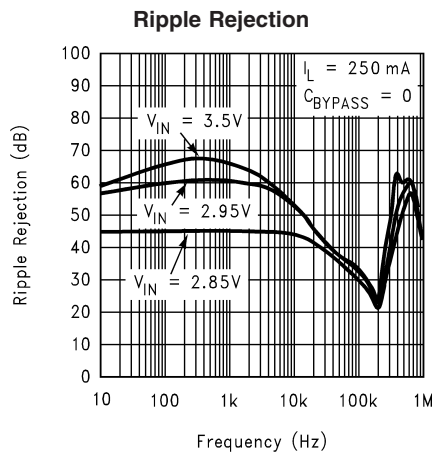
10133940



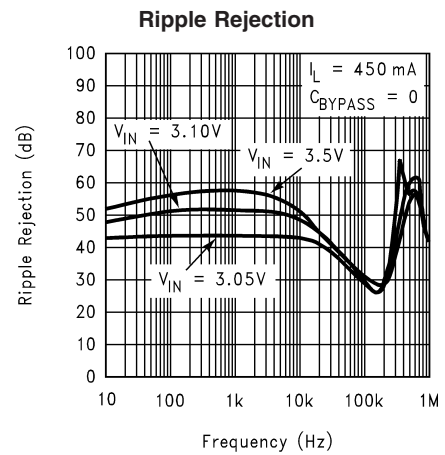
10133941



10133942

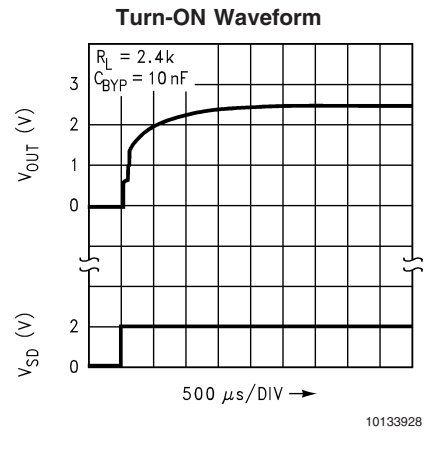
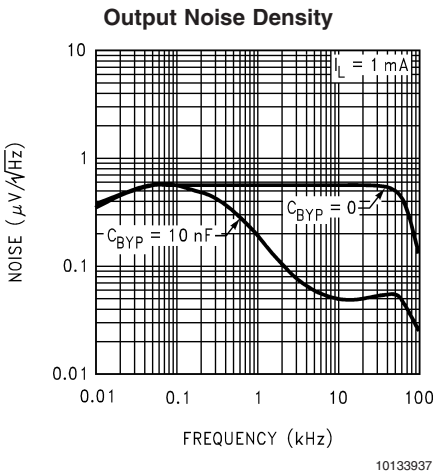
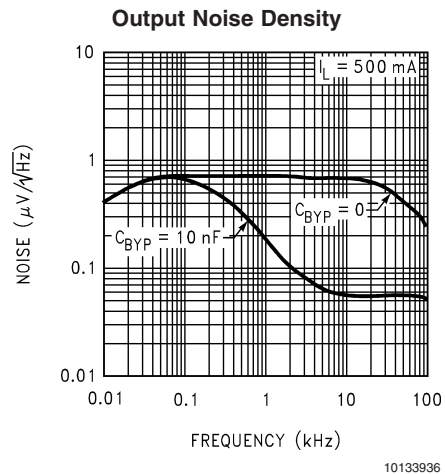
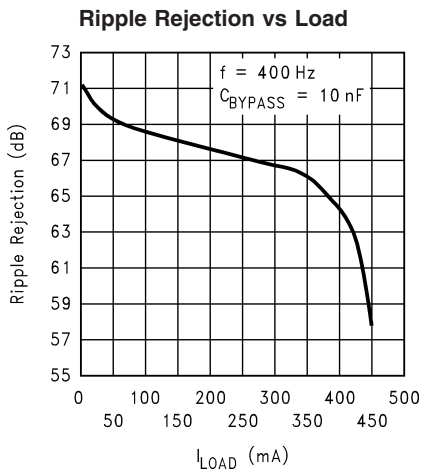
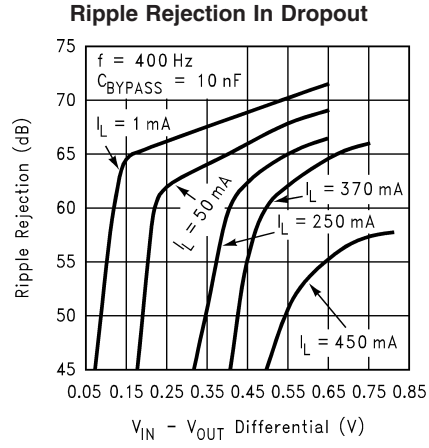
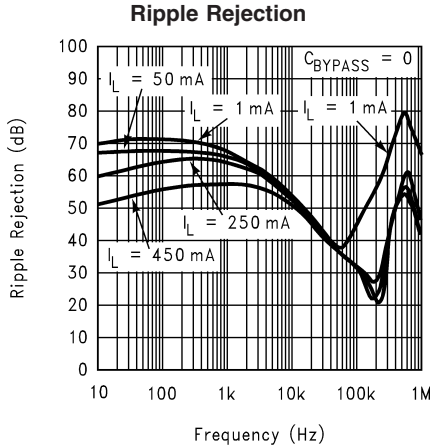


10133943

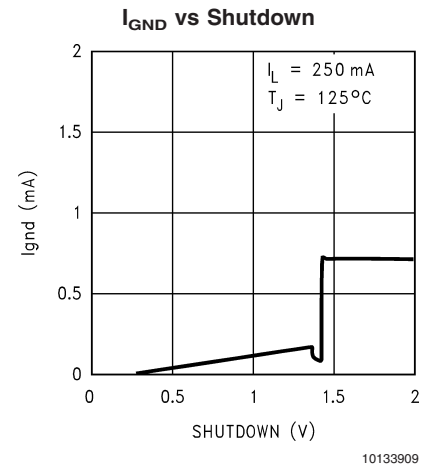
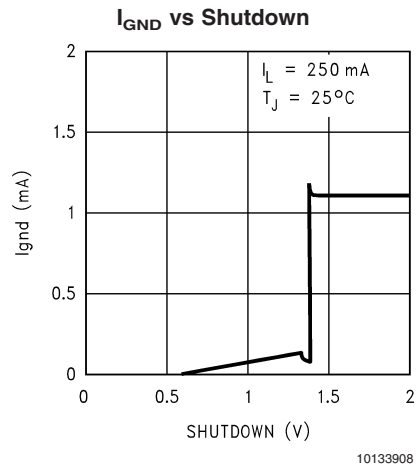
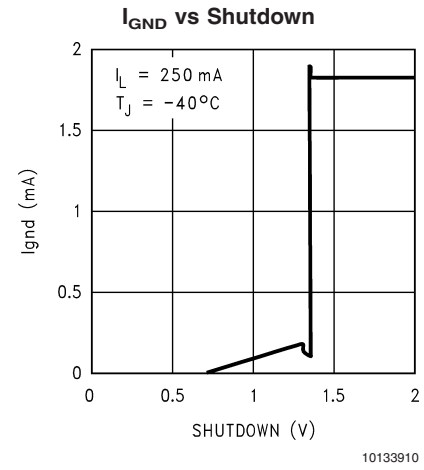
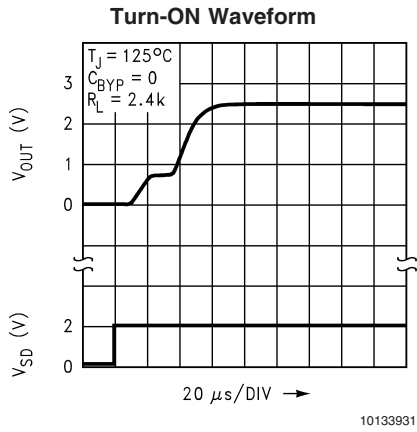
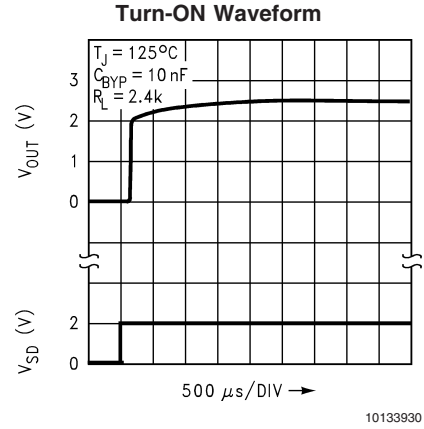
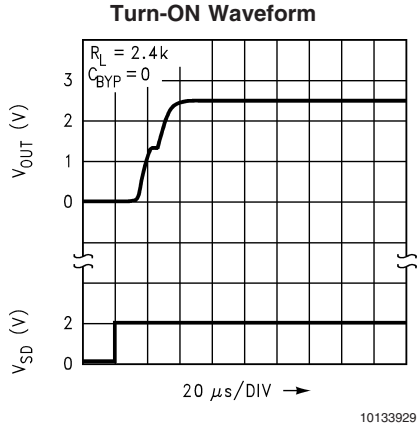


10133944

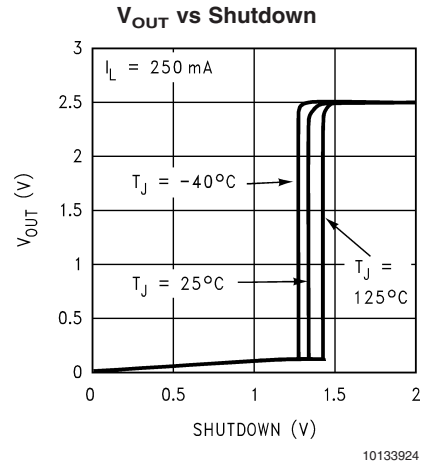
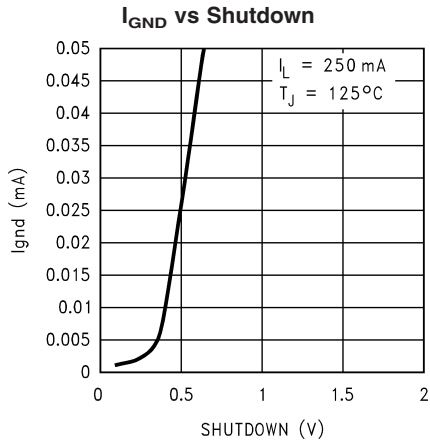
**Typical Performance Characteristics** Unless otherwise specified:  $T_A = 25^\circ\text{C}$ ,  $C_{OUT} = 4.7 \mu\text{F}$ ,  $C_{IN} = 2.2 \mu\text{F}$ , S/D is tied to  $V_{IN}$ ,  $V_{IN} = V_{O(NOM)} + 1\text{V}$ ,  $I_L = 1 \text{mA}$ ,  $V_{OUT} = 2.5\text{V}$ . (Continued)



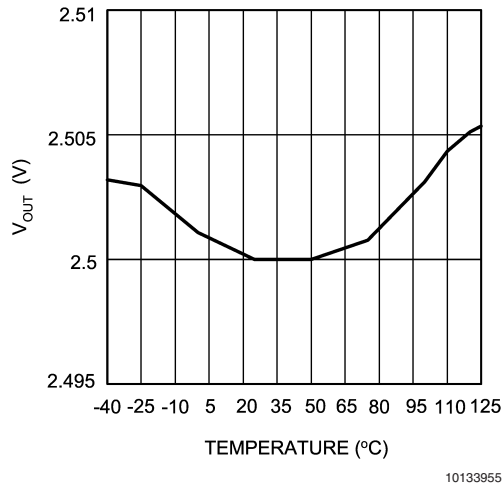
**Typical Performance Characteristics** Unless otherwise specified:  $T_A = 25^\circ\text{C}$ ,  $C_{OUT} = 4.7\ \mu\text{F}$ ,  $C_{IN} = 2.2\ \mu\text{F}$ , S/D is tied to  $V_{IN}$ ,  $V_{IN} = V_{O(NOM)} + 1\text{V}$ ,  $I_L = 1\ \text{mA}$ ,  $V_{OUT} = 2.5\text{V}$ . (Continued)



**Typical Performance Characteristics** Unless otherwise specified:  $T_A = 25^\circ\text{C}$ ,  $C_{OUT} = 4.7 \mu\text{F}$ ,  $C_{IN} = 2.2 \mu\text{F}$ , S/D is tied to  $V_{IN}$ ,  $V_{IN} = V_O(\text{NOM}) + 1\text{V}$ ,  $I_L = 1 \text{mA}$ ,  $V_{OUT} = 2.5\text{V}$ . (Continued)



**Typical Temperature vs  $V_{OUT}$  (LP2989-2.5)**



## Application Hints

### LLP PACKAGE DEVICES

The LP2989 is offered in the 8 lead LLP surface mount package to allow for increased power dissipation compared to the SO-8 and Mini SO-8. For details on thermal performance as well as mounting and soldering specifications, refer to Application Note AN-1187.

For output voltages < 2V, see LP2989LV datasheet.

### EXTERNAL CAPACITORS

Like any low-dropout regulator, the LP2989 requires external capacitors for regulator stability. These capacitors must be correctly selected for good performance.

#### Input Capacitor

An input capacitor whose size is at least 2.2  $\mu\text{F}$  is required between the LP2989 input and ground (the amount of capacitance may be increased without limit).

Characterization testing performed on the LP2989 has shown that if the amount of actual input capacitance drops below about 1.5  $\mu\text{F}$ , an unstable operating condition may result. Therefore, the next larger standard size (2.2  $\mu\text{F}$ ) is specified as the minimum required input capacitance. Capacitor tolerance and temperature variation must be considered when selecting a capacitor (see **Capacitor Characteristics** section) to assure the minimum requirement of 1.5  $\mu\text{F}$  is met over all operating conditions.

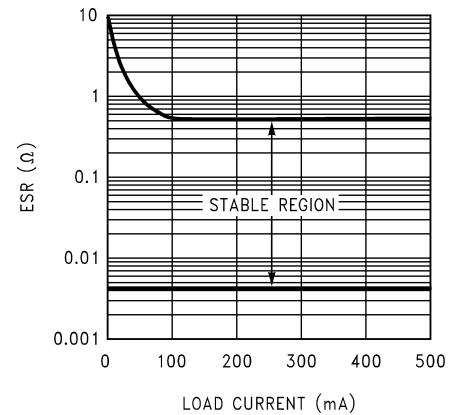
The input capacitor must be located at a distance of not more than 0.5" from the input pin and returned to a clean analog ground. Any good quality ceramic or tantalum may be used for this capacitor, assuming the minimum capacitance requirement is met.

#### Output Capacitor

The LP2989 requires a ceramic output capacitor whose size is at least 4.7 $\mu\text{F}$ . The actual amount of capacitance on the output must never drop below about 3.5 $\mu\text{F}$  or unstable operation may result. For this reason, capacitance tolerance and temperature characteristics must be considered when selecting an output capacitor.

The LP2989 is designed specifically to work with ceramic output capacitors, utilizing circuitry which allows the regulator to be stable across the entire range of output current with an output capacitor whose ESR is as low as 4 m $\Omega$ . It may also be possible to use Tantalum or film capacitors at the output, but these are not as attractive for reasons of size and cost (see next section Capacitor Characteristics).

The output capacitor must meet the requirement for minimum amount of capacitance and also have an ESR (equivalent series resistance) value which is within the stable range. Curves are provided which show the stable ESR range as a function of load current (see ESR graph below).



10133938  
**Stable Region For output Capacitor ESR**

**Important:** The output capacitor must maintain its ESR within the stable region *over the full operating temperature range of the application* to assure stability.

It is important to remember that capacitor tolerance and variation with temperature must be taken into consideration when selecting an output capacitor so that the minimum required amount of output capacitance is provided over the full operating temperature range. (See Capacitor Characteristics section).

The output capacitor must be located not more than 0.5" from the output pin and returned to a clean analog ground.

#### Noise Bypass Capacitor

Connecting a 10 nF capacitor to the Bypass pin significantly reduces noise on the regulator output. However, the capacitor is connected directly to a high-impedance circuit in the bandgap reference.

Because this circuit has only a few microamperes flowing in it, any significant loading on this node will cause a change in the regulated output voltage. For this reason, DC leakage current through the noise bypass capacitor must never exceed 100 nA, and should be kept as low as possible for best output voltage accuracy.

The types of capacitors best suited for the noise bypass capacitor are ceramic and film. High-quality ceramic capacitors with either NPO or COG dielectric typically have very low leakage. 10 nF polypropylene and polycarbonate film capacitors are available in small surface-mount packages and typically have extremely low leakage current.

### CAPACITOR CHARACTERISTICS

#### Ceramic

The LP2989 was designed to work with ceramic capacitors on the output to take advantage of the benefits they offer: for capacitance values in the 4.7  $\mu\text{F}$  range, ceramics are the least expensive and also have the lowest ESR values (which makes them best for eliminating high-frequency noise). The ESR of a typical 4.7  $\mu\text{F}$  ceramic capacitor is in the range of 10 m $\Omega$  to 15 m $\Omega$ , which easily meets the ESR limits required for stability by the LP2989.

One disadvantage of ceramic capacitors is that their capacitance can vary with temperature. Many large value ceramic capacitors ( $\geq 2.2 \mu\text{F}$ ) are manufactured with the Z5U or Y5V

## Application Hints (Continued)

temperature characteristic, which results in the capacitance dropping by more than 50% as the temperature goes from 25°C to 85°C.

This could cause problems if a 4.7  $\mu\text{F}$  capacitor were used on the output since it will drop down to approximately 2.4  $\mu\text{F}$  at high ambient temperatures (which could cause the LP2989 to oscillate). Another significant problem with Z5U and Y5V dielectric devices is that the capacitance drops severely with applied voltage. A typical Z5U or Y5V capacitor can lose 60% of its rated capacitance with half of the rated voltage applied to it.

**For these reasons, X7R and X5R type ceramic capacitors must be used on the input and output of the LP2989**

### Tantalum

Tantalum capacitors are less desirable than ceramics for use as output capacitors because they are typically more expensive when comparing equivalent capacitance and voltage ratings in the 1  $\mu\text{F}$  to 4.7  $\mu\text{F}$  range.

Another important consideration is that Tantalum capacitors have higher ESR values than equivalent size ceramics. This means that while it may be possible to find a Tantalum capacitor with an ESR value within the stable range, it would have to be larger in capacitance (which means bigger and more costly) than a ceramic capacitor with the same ESR value.

It should also be noted that the ESR of a typical Tantalum will increase about 2:1 as the temperature goes from 25°C down to -40°C, so some guard band must be allowed.

Tantalum capacitors may be used on the input as long as the requirement for minimum capacitance is met.

### Film

Polycarbonate and polypropylene film capacitors have excellent electrical performance: their ESR is the lowest of the three types listed, their capacitance is very stable with temperature, and DC leakage current is extremely low.

One disadvantage is that film capacitors are larger in physical size than ceramic or tantalum which makes film a poor choice for either input or output capacitors.

However, their low leakage makes them a good choice for the noise bypass capacitor. Since the required amount of capacitance is only .01  $\mu\text{F}$ , small surface-mount film capacitors are available in this size.

### SHUTDOWN INPUT OPERATION

The LP2989 is shut off by driving the Shutdown input low, and turned on by pulling it high. If this feature is not to be used, the Shutdown input should be tied to  $V_{\text{IN}}$  to keep the regulator output on at all times.

To assure proper operation, the signal source used to drive the Shutdown input must be able to swing above and below the specified turn-on/turn-off voltage thresholds listed in the Electrical Characteristics section under  $V_{\text{ON/OFF}}$ .

To prevent mis-operation, the turn-on (and turn-off) voltage signals applied to the Shutdown input must have a slew rate which is  $\geq 40 \text{ mV}/\mu\text{s}$ .

**CAUTION:** the regulator output voltage can not be guaranteed if a slow-moving AC (or DC) signal is applied that is in the range between the specified turn-on and turn-off voltages listed under the electrical specification  $V_{\text{ON/OFF}}$  (see Electrical Characteristics).

### REVERSE INPUT-OUTPUT VOLTAGE

The PNP power transistor used as the pass element in the LP2989 has an inherent diode connected between the regulator output and input.

During normal operation (where the input voltage is higher than the output) this diode is reverse-biased.

However, if the output is pulled above the input, this diode will turn ON and current will flow into the regulator output.

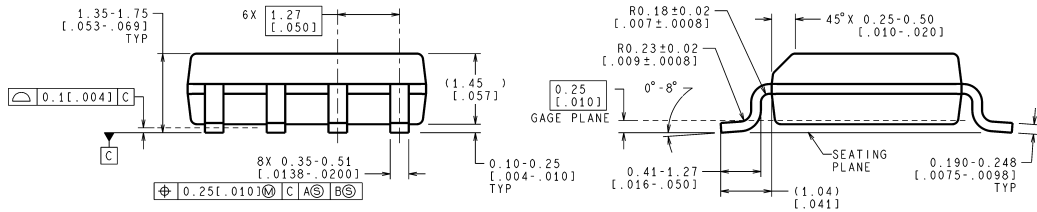
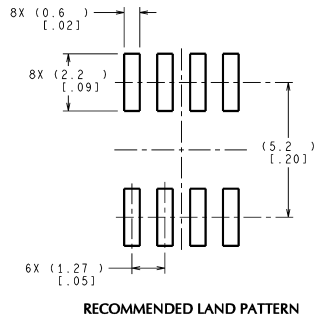
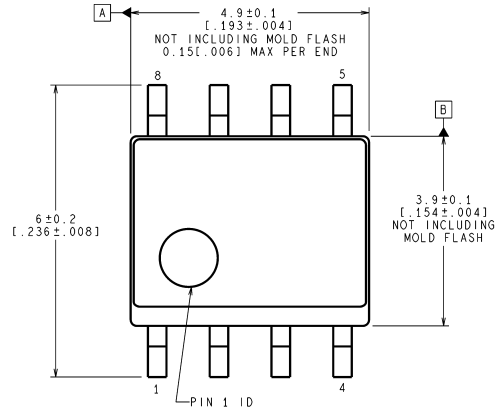
In such cases, a parasitic SCR can latch which will allow a high current to flow into  $V_{\text{IN}}$  (and out the ground pin), which can damage the part.

In any application where the output may be pulled above the input, an external Schottky diode must be connected from  $V_{\text{IN}}$  to  $V_{\text{OUT}}$  (cathode on  $V_{\text{IN}}$ , anode on  $V_{\text{OUT}}$ ), to limit the reverse voltage across the LP2989 to 0.3V (see Absolute Maximum Ratings).



# Physical Dimensions inches (millimeters)

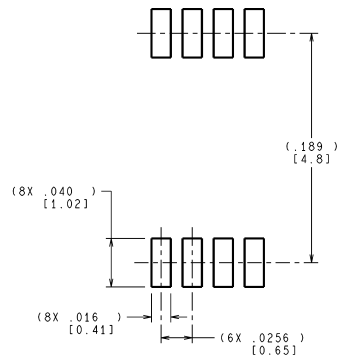
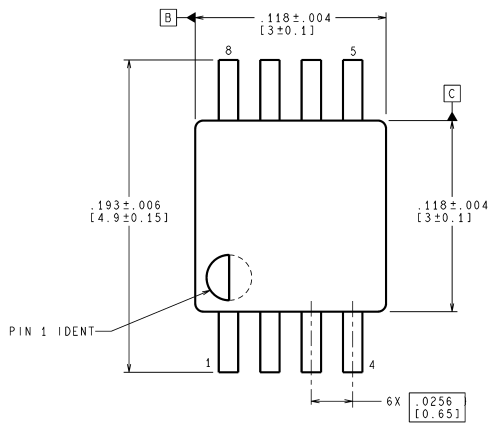
unless otherwise noted



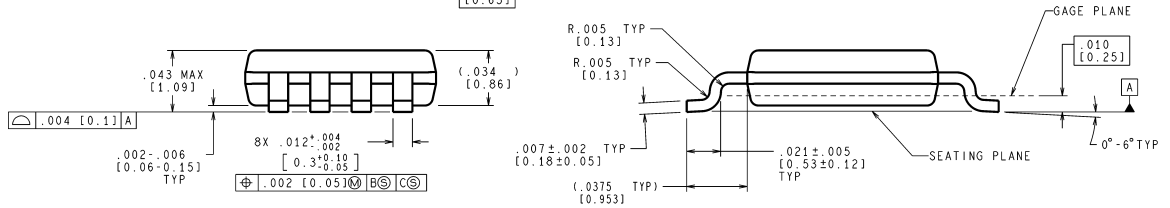
CONTROLLING DIMENSION IS MILLIMETER  
VALUES IN [ ] ARE INCHES  
DIMENSIONS IN ( ) FOR REFERENCE ONLY

M08A (Rev K)

## SO-8 Package NS Package Number M08A



LAND PATTERN RECOMMENDATION

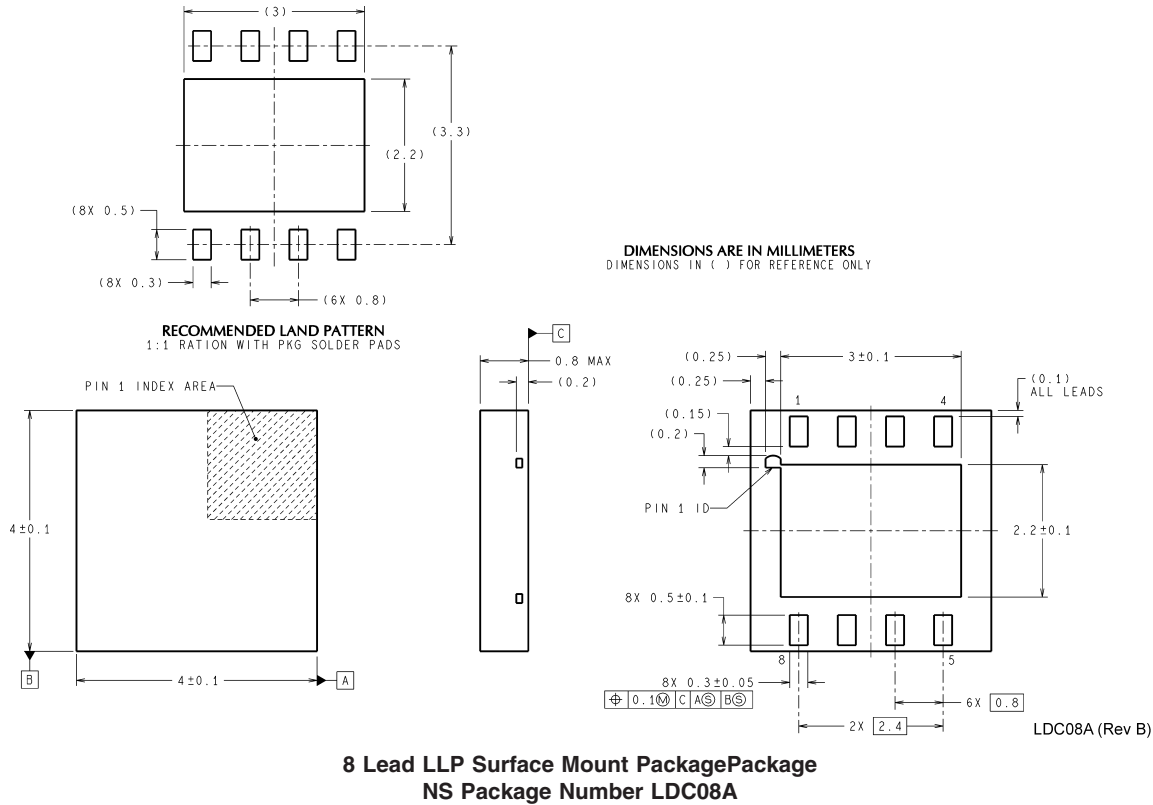


CONTROLLING DIMENSION IS INCH  
VALUES IN [ ] ARE MILLIMETERS

MUA08A (Rev E)

## Mini SO-8 Package NS Package Number MUA08A

**Physical Dimensions** inches (millimeters) unless otherwise noted (Continued)



National does not assume any responsibility for use of any circuitry described, no circuit patent licenses are implied and National reserves the right at any time without notice to change said circuitry and specifications.

For the most current product information visit us at [www.national.com](http://www.national.com).

**LIFE SUPPORT POLICY**

NATIONAL'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT AND GENERAL COUNSEL OF NATIONAL SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

**BANNED SUBSTANCE COMPLIANCE**

National Semiconductor manufactures products and uses packing materials that meet the provisions of the Customer Products Stewardship Specification (CSP-9-111C2) and the Banned Substances and Materials of Interest Specification (CSP-9-111S2) and contain no "Banned Substances" as defined in CSP-9-111S2.

**National Semiconductor**  
**Americas Customer Support Center**  
 Email: [new.feedback@nsc.com](mailto:new.feedback@nsc.com)  
 Tel: 1-800-272-9959

**National Semiconductor**  
**Europe Customer Support Center**  
 Fax: +49 (0) 180-530 85 86  
 Email: [europe.support@nsc.com](mailto:europe.support@nsc.com)  
 Deutsch Tel: +49 (0) 69 9508 6208  
 English Tel: +44 (0) 870 24 0 2171  
 Français Tel: +33 (0) 1 41 91 8790

**National Semiconductor**  
**Asia Pacific Customer Support Center**  
 Email: [ap.support@nsc.com](mailto:ap.support@nsc.com)

**National Semiconductor**  
**Japan Customer Support Center**  
 Fax: 81-3-5639-7507  
 Email: [jpn.feedback@nsc.com](mailto:jpn.feedback@nsc.com)  
 Tel: 81-3-5639-7560

## IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

TI products are not authorized for use in safety-critical applications (such as life support) where a failure of the TI product would reasonably be expected to cause severe personal injury or death, unless officers of the parties have executed an agreement specifically governing such use. Buyers represent that they have all necessary expertise in the safety and regulatory ramifications of their applications, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of TI products in such safety-critical applications, notwithstanding any applications-related information or support that may be provided by TI. Further, Buyers must fully indemnify TI and its representatives against any damages arising out of the use of TI products in such safety-critical applications.

TI products are neither designed nor intended for use in military/aerospace applications or environments unless the TI products are specifically designated by TI as military-grade or "enhanced plastic." Only products designated by TI as military-grade meet military specifications. Buyers acknowledge and agree that any such use of TI products which TI has not designated as military-grade is solely at the Buyer's risk, and that they are solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI products are neither designed nor intended for use in automotive applications or environments unless the specific TI products are designated by TI as compliant with ISO/TS 16949 requirements. Buyers acknowledge and agree that, if they use any non-designated products in automotive applications, TI will not be responsible for any failure to meet such requirements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

### Products

Audio	<a href="http://www.ti.com/audio">www.ti.com/audio</a>
Amplifiers	<a href="http://amplifier.ti.com">amplifier.ti.com</a>
Data Converters	<a href="http://dataconverter.ti.com">dataconverter.ti.com</a>
DLP® Products	<a href="http://www.dlp.com">www.dlp.com</a>
DSP	<a href="http://dsp.ti.com">dsp.ti.com</a>
Clocks and Timers	<a href="http://www.ti.com/clocks">www.ti.com/clocks</a>
Interface	<a href="http://interface.ti.com">interface.ti.com</a>
Logic	<a href="http://logic.ti.com">logic.ti.com</a>
Power Mgmt	<a href="http://power.ti.com">power.ti.com</a>
Microcontrollers	<a href="http://microcontroller.ti.com">microcontroller.ti.com</a>
RFID	<a href="http://www.ti-rfid.com">www.ti-rfid.com</a>
OMAP Mobile Processors	<a href="http://www.ti.com/omap">www.ti.com/omap</a>
Wireless Connectivity	<a href="http://www.ti.com/wirelessconnectivity">www.ti.com/wirelessconnectivity</a>

### Applications

Communications and Telecom	<a href="http://www.ti.com/communications">www.ti.com/communications</a>
Computers and Peripherals	<a href="http://www.ti.com/computers">www.ti.com/computers</a>
Consumer Electronics	<a href="http://www.ti.com/consumer-apps">www.ti.com/consumer-apps</a>
Energy and Lighting	<a href="http://www.ti.com/energy">www.ti.com/energy</a>
Industrial	<a href="http://www.ti.com/industrial">www.ti.com/industrial</a>
Medical	<a href="http://www.ti.com/medical">www.ti.com/medical</a>
Security	<a href="http://www.ti.com/security">www.ti.com/security</a>
Space, Avionics and Defense	<a href="http://www.ti.com/space-avionics-defense">www.ti.com/space-avionics-defense</a>
Transportation and Automotive	<a href="http://www.ti.com/automotive">www.ti.com/automotive</a>
Video and Imaging	<a href="http://www.ti.com/video">www.ti.com/video</a>

TI E2E Community Home Page

[e2e.ti.com](http://e2e.ti.com)

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265  
Copyright © 2011, Texas Instruments Incorporated