



T-75-45-05

DS26F32C/DS26F32M Quad Differential Line Receiver

General Description

The DS26F32 is a quad differential line receiver designed to meet the requirements of EIA Standards RS-422 and RS-423, and Federal Standards 1020 and 1030 for balanced and unbalanced digital data transmission.

The DS26F32 offers improved performance due to the use of state-of-the-art L-FAST bipolar technology. The L-FAST technology allows for higher speeds and lower currents by utilizing extremely short gate delay times. Thus, the DS26F32 features lower power, extended temperature range, and improved specifications.

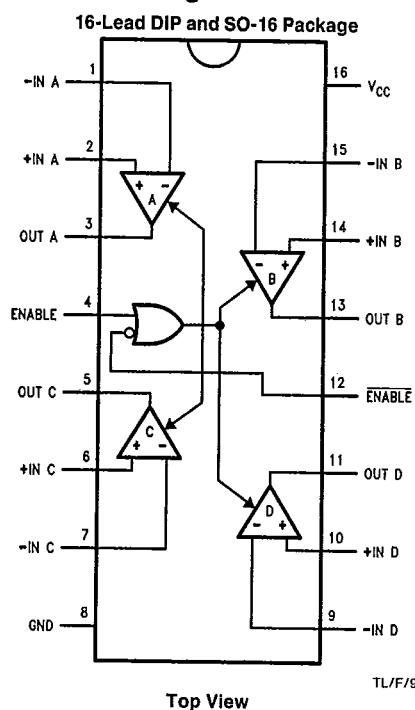
The device features an input sensitivity of 200 mV over the input range of $\pm 7.0\text{V}$. The DS26F32 provides an enable function common to all four receivers and TRI-STATE® outputs with 8.0 mA sink capability. Also, a fail-safe input/output relationship keeps the outputs high when the inputs are open.

The DS26F32 offers optimum performance when used with the DS26F31 Quad Differential Line Driver.

Features

- Low power version
- Input voltage range of $\pm 7.0\text{V}$ (differential or common mode) $\pm 0.2\text{V}$ sensitivity over the input voltage range
- Meets all the requirements of EIA standards RS-422 and RS-423
- Input impedance (18k typical)
- 30 mV input hysteresis
- Operation from single +5.0V supply
- Fail-safe input/output relationship. Output always high when inputs are open
- TRI-STATE drive, with choice of complementary output enables, for receiving directly onto a data bus
- Propagation delay 15 ns typical
- Advanced low power Schottky processing
- Extended temperature range

Connection Diagram



Function Table (Each Receiver)

Differential Inputs	Enables	Outputs
A-B	E \bar{E}	V
$V_{ID} \geq 0.2\text{V}$	H X	H
	X L	H
$V_{ID} \leq -0.2\text{V}$	H X	L
	X L	L
X	L H	Z

H = High Level
L = Low Level
X = Immaterial

Order Number DS26F32CJ or DS26F32MJ
See NS Package Number* J16A

Order Number DS26F32CM
See NS Package Number M16A

Order Number DS26F32CN
See NS Package Number N16A

*For most current package information contact product marketing.

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DS26F32C/DS26F32M

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Storage Temperature Range

Ceramic DIP -65°C to $+175^{\circ}\text{C}$
Molded DIP and SO-16 -65°C to $+150^{\circ}\text{C}$

Operating Temperature Range

DS26F32M -55°C to $+125^{\circ}\text{C}$
DS26F32C 0°C to $+70^{\circ}\text{C}$

Lead Temperature

Ceramic DIP (soldering, 60 sec) 300°C
Molded DIP and SO-16 (soldering, 10 sec) 265°C

Maximum Power Dissipation* at 25°C

Cavity Package 1500 mW
Molded Package 1040 mW
SO Package 960 mW

*Derate cavity package 10 mW/ $^{\circ}\text{C}$ above 25°C ; derate molded DIP package 8.3 mW/ $^{\circ}\text{C}$ above 25°C ; derate SO package 7.7 mW/ $^{\circ}\text{C}$ above 25°C .

Supply Voltage	7.0V
Common Mode Voltage Range	$\pm 25\text{V}$
Differential Input Voltage	$\pm 25\text{V}$
Enable Voltage	7.0V
Output Sink Current	50 mA

Operating Range

DS26F32C	Temperature	0°C to $+70^{\circ}\text{C}$
	Supply Voltage	4.75V to 5.25V
DS26F32M	Temperature	-55°C to $+125^{\circ}\text{C}$
	Supply Voltage	4.5V to 5.5V

Electrical Characteristics Over operating range, unless otherwise specified (Notes 2 and 3)

Symbol	Parameter	Conditions		Min	Typ	Max	Units
V_{TH}	Differential Input Voltage	$-7.0\text{V} \leq V_{CM} \leq +7.0\text{V}$, $V_O = V_{OL}$ or V_{OH}		-0.2	± 0.06	+0.2	V
R_I	Input Resistance	$-15\text{V} \leq V_{CM} \leq +15\text{V}$, One Input AC Ground		14	18		k Ω
I_I	Input Current (under Test)	$V_I = +15\text{V}$, Other Input $-15\text{V} \leq V_I \leq +15\text{V}$				2.3	mA
		$V_I = -15\text{V}$, Other Input $-15\text{V} \leq V_I \leq +15\text{V}$				-2.8	
V_{OH}	Output Voltage HIGH	$V_{CC} = \text{Min}$, $\Delta V_I = +1.0\text{V}$, $V_{ENABLE} = 0.8\text{V}$, $I_{OH} = -440\text{ }\mu\text{A}$	0°C to $+70^{\circ}\text{C}$	2.8	3.4		V
			-55°C to $+125^{\circ}\text{C}$	2.5	3.4		
V_{OL}	Output Voltage LOW	$V_{CC} = \text{Min}$, $\Delta V_I = -1.0\text{V}$, $V_{ENABLE} = 0.8\text{V}$	$I_{OL} = 4.0\text{ mA}$			0.4	V
			$I_{OL} = 8.0\text{ mA}$			0.45	
V_{IL}	Enable Voltage LOW					0.8	V
V_{IH}	Enable Voltage HIGH			2.0			V
V_{IC}	Enable Clamp Voltage	$V_{CC} = \text{Min}$, $I_I = -18\text{ mA}$				-1.5	V
I_{OZ}	Off State (High Impedance) Output Current	$V_{CC} = \text{Max}$	$V_O = 2.4\text{V}$			20	μA
			$V_O = 0.4\text{V}$			-20	
I_{IL}	Enable Current LOW	$V_I = 0.4\text{V}$			-0.2	-0.36	mA
I_{IH}	Enable Current HIGH	$V_I = 2.7\text{V}$			0.5	10	μA
I_I	Enable Input High Current	$V_I = 5.5\text{V}$			1.0	50	μA
I_{OS}	Output Short Circuit Current	$V_O = 0\text{V}$, $V_{CC} = \text{Max}$, (Note 4) $\Delta V_I = +1.0\text{V}$		-15	-50	-85	mA
I_{CC}	Supply Current	$V_{CC} = \text{Max}$, All $V_I = \text{GND}$, Outputs Disabled			30	50	mA
V_{HYST}	Input Hysteresis	$T_A = 25^{\circ}\text{C}$, $V_{CC} = 5.0\text{V}$, $V_{CM} = 0\text{V}$			30		mV

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The tables of "Electrical Characteristics" provide conditions for actual device operation.

Note 2: Unless otherwise specified min/max limits apply across the -55°C to $+125^{\circ}\text{C}$ temperature range for the DS26F32M and across the 0°C to $+70^{\circ}\text{C}$ range for the DS26F32C. All typicals are given for $V_{CC} = 5\text{V}$ and $T_A = 25^{\circ}\text{C}$.

Note 3: All currents into the device pins are positive; all currents out of the device pins are negative. All voltages are reference to ground unless otherwise specified.

Note 4: Only one output at a time should be shorted.

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Switching Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Units
t_{PLH}	Input to Output	$T_A = 25^{\circ}\text{C}, V_{CC} = 5.0\text{V}, C_L = 15 \text{ pF, See Test Circuit (Figure 2)}$		15	22	ns
t_{PHL}	Input to Output	$T_A = 25^{\circ}\text{C}, V_{CC} = 5.0\text{V}, C_L = 15 \text{ pF, See Test Circuit (Figure 2)}$		15	22	ns
t_{LZ}	Enable to Output	$T_A = 25^{\circ}\text{C}, V_{CC} = 5.0\text{V}, C_L = 15 \text{ pF, See Test Circuit (Figure 2)}$		14	18	ns
t_{HZ}	Enable to Output	$T_A = 25^{\circ}\text{C}, V_{CC} = 5.0\text{V}, C_L = 15 \text{ pF, See Test Circuit (Figure 2)}$		15	20	ns
t_{ZL}	Enable to Output	$T_A = 25^{\circ}\text{C}, V_{CC} = 5.0\text{V}, C_L = 15 \text{ pF, See Test Circuit (Figure 2)}$		13	18	ns
t_{ZH}	Enable to Output	$T_A = 25^{\circ}\text{C}, V_{CC} = 5.0\text{V}, C_L = 15 \text{ pF, See Test Circuit (Figure 2)}$		12	16	ns

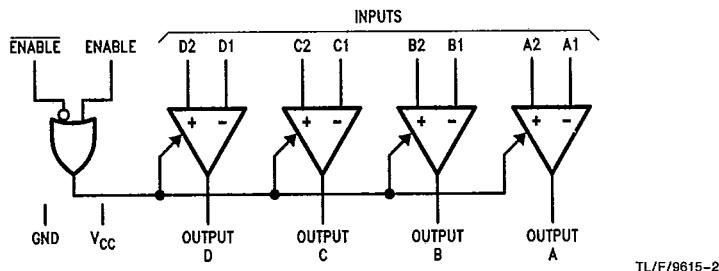


FIGURE 1. Logic Symbol

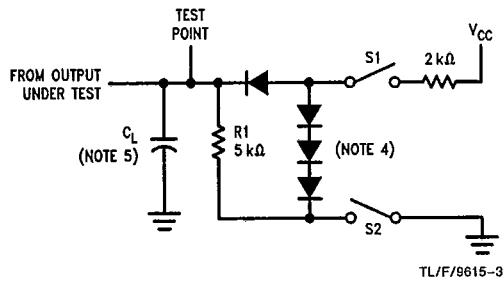


FIGURE 2. Load Test Circuit for Three-State Outputs

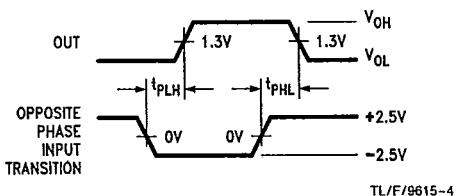
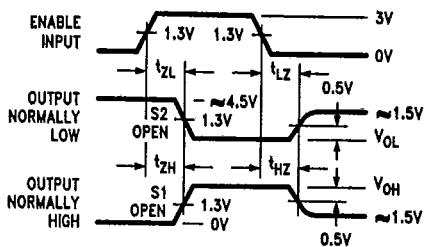


FIGURE 3. Propagation Delay (Notes 1, 2 and 3)

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DS26F32C/DS26F32M



- Note 1: Diagram shown for ENABLE Low.
 Note 2: S1 and S2 of Load Circuit are closed except where shown.
 Note 3: Pulse Generator of all Pulses: Rate \leq 1.0 MHz, $Z_0 = 50\Omega$, $t_r \leq 6.0$ ns, $t_f \leq 6.0$ ns.
 Note 4: All diodes are IN916 or IN3064.
 Note 5: C_L Includes probe and jig capacitance.

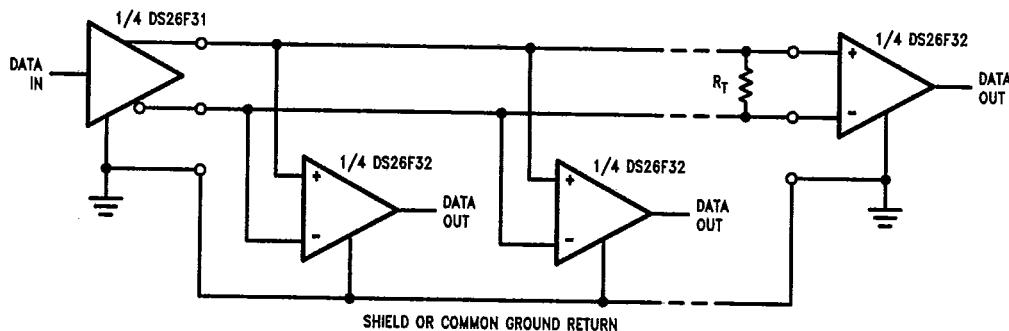
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FIGURE 4. Enable and Disable Times (Notes 1, 2 and 3)**Typical Application**

FIGURE 5

TL/F/9615-6

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