

# Libero IDE

Microsemi's Libero ® IDE software release for designing with Microsemi [Rad-Tolerant FPGAs](#), [Antifuse FPGAs](#) and [Legacy & Discontinued Flash FPGAs](#) and managing the entire design flow from design entry, synthesis and simulation, through place-and-route, timing and power analysis. [PCN 1108: Silicon Family Support in Libero IDE](#).

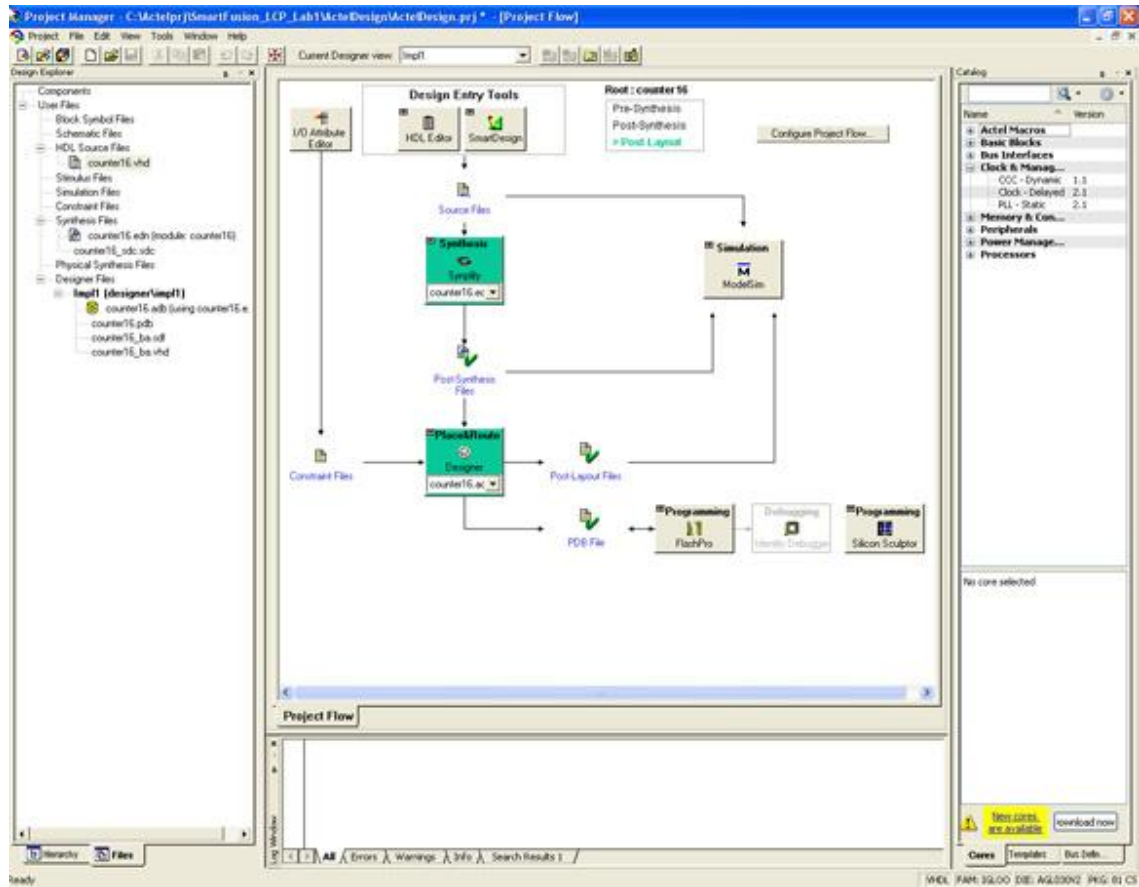
For other families please use [Libero SoC Design Suite](#) or [Libero SoC PolarFire](#), see Device Support tab for details.

**Note: Libero license options are changing as indicated in Customer Notification [CN17012](#). These changes came into effect with Libero SoC v11.8 released on 13<sup>th</sup> March, 2017.**

## Libero IDE Software Features:

- Powerful project and design flow management
- Full suite of integrated design entry tools and methodologies:
  - SmartDesign graphical SoC design creation with automatic abstraction to HDL
  - IP Core Catalog and configuration
  - User-defined block creation flow for design re-use
- [Synplify Pro ME](#) synthesis fully optimizes Microsemi FPGA device performance and area utilization
- [Symphony Model Compiler](#) ME performs high-level synthesis optimizations within a Simulink® environment
- [Modelsim](#) ME VHDL or Verilog behavioral, post-synthesis and post-layout simulation capability
- Physical design implementation, floorplanning, physical constraints, and layout
- Timing-driven and power-driven place-and-route
- SmartTime environment for timing constraint management and analysis
- SmartPower provides comprehensive power analysis for actual and "what if" power scenarios
- Interface to [FlashPro](#) programmers
- Post-route On Chip [Debug Tools](#) and [Identify ME](#) debugging software for Microsemi flash designs
- [Silicon Explorer II](#) debugging software for Microsemi antifuse designs

## Libero IDE Project Manager



## Supported Product Families

Product families supported in Libero IDE are given below

- ACT1
- ACT2/1200XL
- ACT3
- 3200DX
- 40MX
- 42MX
- SX/SX-A (including RTSX/-S/-SU)
- eX
- Axcelerator® (including RTAX™-S, RTAX-D)
- ProASIC (aka 500K)
- ProASICPLUS (aka APA)
- ProAsic®3E is available for RT prototyping only (for designs targetted to ProASIC®3E, use Libero SoC)

For licensing requirements on these product families, please refer to the Licensing tab.

## Supported Platforms

Microsemi tests and supports the latest releases on the specific operating systems shown in the chart below. We are committed to resolving problems encountered by customers on these supported operating systems. We do not support untested operating systems or versions.

### Software Tools

#### Operating Systems LiberoFlashPro

Windows 10 <sup>3</sup>	✓	✓
Windows 7 <sup>1</sup>	✓	✓
Windows XP Pro SP3 <sup>1</sup>	✓	✓
RHEL 5 (Tikanga) <sup>1</sup>	✓	N/A
RHEL 6 (Tikanga) <sup>2</sup>	✓	N/A

1. Tools are tested on both 32-bit and 64-bit operating systems.
2. RHEL 6 is supported on 64-bit operating systems only.
3. Windows 10 is supported on 64-bit operating systems.

### FlashPro Programming Software

#### Operating Systems FlashPro4 & 3<sup>1,3</sup>FlashPro Lite<sup>4</sup>

Windows 10	✓	N/A
Windows 7	✓	✓ <sup>2</sup>
Windows XP Pro SP3	✓	✓ <sup>1</sup>

Notes:

1. Both 32-bit and x64 operating systems are supported for USB. The driver delivered with Libero v9.0 SP1 or FlashPro v9.0 SP1 or later is required for programmers connected to x64 systems.
2. 32-bit operating systems only.
3. FlashPro3 was discontinued in 2009 and replaced with FlashPro4. For more information, read the product [discontinuation notification](#).
4. FlashPro Lite supports only the ProASIC<sup>PLUS</sup> family. Parallel Port is not yet available for x64 systems.

### ***Minimum System Requirements***

<b>Software/Platform</b>	<b>Disk Space*</b>	<b>RAM</b>	<b>License Required</b>
Libero IDE	4 GB	See charts below	Yes
Libero IDE SA	1.5 GB	See charts below	Yes
SoftConsole	400 MB	128 MB	No
FlashPro Standalone	120 MB	256 MB	No

*\* Maximum disk space for installation, assuming that all family device libraries are installed, and all IP Cores loaded to the IP Catalog. .*

### ***Recommended Memory Requirements***

<b>Product Family</b>	<b>Device</b>	<b>RAM</b>
RTAX-S and RTAX-DSP	Devices up to RTAX2000	1 GB
RTAX-S and RTAX-DSP	RTAX4000S, RTAX4000D	3 GB*
Axcelerator	All Devices	1 GB
SX-A / eX / MX	All Devices	256 MB
ProASIC <sup>PLUS</sup>	APA1000	1 GB