TranSwitch Corporation

Index of Product Information on CD-ROM Disk Ed. 6, April 1997

This Index is stored on file DOCINDEX.PDF, which is located in the root directory of the TranSwitch Product Information CD-ROM disk. It can only be accessed with an Adobe Acrobat Reader.

Important Notice:
Please use a file viewer, editor or word processor to read the text file named readme.txt in the root directory of this CD-ROM disk for a statement of the terms and conditions that apply to its use. This file also provides instructions for the installation of Adobe Acrobat Reader software.

Instructions:

All documents contained in this CD-ROM disk are stored as Adobe Portable Document Format (PDF) files with file name extension ".PDF". To select and hyperlink to the first page of any of the PDF document files listed below, position your cursor on or near the "■" link square located to the left of the listing for the desired document and then click the left mouse button. File names are included in parentheses in the listings for reference. Documents which have been added, updated or corrected compared with the prior edition of this CD-ROM disk have their file names shown in **bold font.** New and updated documents released before the next CD-ROM edition may be found on our Web site.*

An Adobe Acrobat Reader software program is needed to view and print these PDF files. Readers for Windows, DOS, Macintosh and UNIX platforms are provided on this CD-ROM. They include Version 2.1 Readers for Windows, Macintosh, and UNIX (Sun/Solaris/HP), as provided on earlier editions of this CD-ROM, and improved Version 3.0 Readers. Version 2.1 and later Readers provide sound listening capability when using Windows or Macintosh platforms, or Sun workstation platforms that receive sound files with .au sound file format. Documents on this CD-ROM with sound tracks (voice annotation) are identified in this Index. Version 1.0 Readers for DOS and UNIX (SGI) are also included on this CD-ROM. Reader software may also be obtained directly from Adobe's Internet World Wide Web site, which is located at address http://www.adobe.com, from Adobe's Bulletin Board System at (206) 623-6984 (settings N-8-1-FD), or from Adobe's Acrobat CD Sampler CD-ROM disks.

*Note: Hyperlinking from one document to another is a capability provided only in Version 2.1 / 3.0 Readers. Users of a Version 1.0 Reader cannot click on the link squares, but must open document files by using the Reader's File-Open menu and selecting the file name of the desired file. The directories in which the .PDF files for the documents are located are shown in file \TREELIST.PDF, listed below:

■ CD-ROM directory tree and file list indicating file locations (**TREELIST.PDF**).

Additional information on TranSwitch products is available upon request from our Marketing Department, which may be contacted via mail, telephone, fax, E-mail or Web Site, as shown above.

What's New - Data Networking and Access Products (formerly ATM Products)

Welcome

TranSwitch is moving to focus more on end markets and applications, which is the reason for our change to "Data Networking and Access" from "ATM". TranSwitch has always prided itself on providing complete solutions for its customers, and that will continue into the future with our Data Networking and Access solutions. In the future, TranSwitch will be supporting both ATM and other data networking technologies which are consistent with the products our customers are developing for the largest and fastest growing communications markets. Access products will also be a major thrust for TranSwitch, and we expect to introduce several innovative solutions in this area in 1997 and 1998.

In addition, you will notice that our new products will be more complete solutions, with more application notes, device driver software, development boards, and other support tools that will enable customers to get to market faster. We expect to announce multiple partnerships over the next six months that will bring in additional support for our products, mostly focused on software.

New Products and Documentation

On this Edition 6 of our CD-ROM, you will notice some notable additions and updates to our Data Networking and Access documentation, including:

- SALI-25C Data Sheet (*PRELIMINARY* Ed. 2, formerly *PRODUCT PREVIEW* Ed. 1)

- SARA-2 Family of ATM Segmentation and Reassembly Products (formerly SARA II IC only)

The SALI-25C is a six-channel ATM25 product targeted at ATM LAN applications. Its physical layer functionality and robust queuing support certainly set the SALI-25C apart from the competition.

The SARA-2 Product Family includes two solutions for high performance ATM Segmentation and Reassembly applications. These solutions consist of two versions of microcode that both run on a common SARA-2 ATM Cell Processing IC. The power of including a MIPS-based RISC core within the SARA-2 is evident in that we can offer two distinct ATM solutions based on the same IC. We will be introducing more documentation on the SARA-2 family over the next three months.

Please take a look at the Product Information documentation on the SALI-25C VLSI device and the SARA-2 product family. Of course, you can get the absolute latest information and updates from our Web Site at http://www.transwitch.com.

TranSwitch Corporation Information

- TranSwitch Corporation - Corporate Profile and Mission (CORP_PRO.PDF).

- North America Sales Operation - contact information (**NA_SALES.PDF**).

- International Sales Operation - contact information (**IN_SALES.PDF**).

Voice Annotated Documents

■ Technology Seminar, Voice Annotated Edition, Ed. 1 Jan 96 (SEMI1NS_.PDF). For additional detail please refer to the Technology Information section on this page.

This is the first of a series of voice annotated documents that will be featured in TranSwitch Product Information CD-ROMs. The voice sound tracks are designed to "play" on Windows, Macintosh and Sun Workstation platforms having sound capability.

The Technology Seminar presents an overview of voice networks (telephony) and data networks (datacom) --- including (i) how these networks are structured, (ii) why they have been structured in this way, (iii) how they have evolved over time, and (iv) the likely future evolution of these networks. The Seminar also identifies application opportunities where TranSwitch VLSI devices can be used in network equipment supplied by original equipment manufacturers (OEMs). Voice sound tracks are linked to sixty-six pages in this one hundred twenty-five page seminar --- for a total playback time of approximately 75 minutes.

The Technology Seminar is also available as a full-text edition without voice narration. Please refer to the Technology Information section on this page.

Technology Information

■ Technology Seminar - Voice Networks and Data Networks, full-text edition, Ed. 1 Jan 96 (SEMI1_S_.PDF).

■ Technology Seminar - Voice Networks and Data Networks, voice annotated edition, Ed. 1 Jan 96 (SEMI1NS_.PDF). *This document includes icons that provide links to sound files containing explanatory narrative. In order to use these links successfully, the computer employed to read this CD-ROM disk must be equipped with appropriate hardware and software for sound reproduction, as indicated in the plain text file readme.txt located in the root directory of this CD-ROM disk.*

■ Draft Technology Standard - ATM Bus on VME, Draft 0.2 Mar 97 (VITA02S_.PDF).
The ATM Bus (AB) on VME is a TranSwitch-designed low-cost structure for building ATM access systems using the Bus User Port device. Using AB architecture and the TranSwitch Bus User Port, manufacturers can design and build versatile, cost-effective ATM access and switching products.

Bilingual Chinese/English Documents

■ TranSwitch VLSI Products, Ed. 1 Feb 96 (CHINA962.PDF)

This is the first of a series of bilingual Chinese/English documents that will be included in TranSwitch Product Information CD-ROMs. This 50-page document includes a technical presentation on Synchronous Digital Hierarchy (SDH), an overview on how TranSwitch VLSI devices can be used in SDH network equipment, and information about TranSwitch Corporation.

TranSwitch *communicator* **Quarterly Newsletters**

- TranSwitch *communicator* - Winter 1997 edition **(COM97WIN.PDF)**.
  Includes technical articles - SDH and ATM - *Friend or Foe*?
  Emerging Requirements for Wide Area Access (Seminar)

- TranSwitch *communicator* - Autumn 1996 edition **(COM96AUT.PDF)**.
  Includes technical article - A New Duo of Quads (Quad E1 Mapper and Framer devices)

- TranSwitch *communicator* - Summer 1996 edition **(COM96SUM.PDF)**.
  Includes technical articles - Segmentation and Reassembly - The Next Generation
  Voice Over ATM (Seminar)

- TranSwitch *communicator* - Spring 1996 edition **(COM96SPR.PDF)**.
  Includes technical article - ATM and SONET in the Internet

- TranSwitch *communicator* - Winter 1996 edition (COM96WIN.PDF).
  Includes technical article - SONET / SDH and ATM: Future Transport Multiplexer

- TranSwitch *communicator* - Autumn 1995 edition (COM95AUT.PDF).
  Includes technical article - COBRA Enables Full Potential of ATM

- TranSwitch *communicator* - Summer 1995 edition (COM95SUM.PDF).
  Includes technical article - L3M: DS3 / E3 Multipurpose Mapper Introduced

- TranSwitch *communicator* - Spring 1995 edition (COM95SPR.PDF).
  Includes technical articles - Quad DS1 Framer Introduced
  Enhanced Devices Aid ATM Networks: SARA-S and SARA-R
  Offer Higher Speed, Lower Power

- TranSwitch *communicator* - Winter 1995 edition (COM95WIN.PDF).
  Includes technical article - CUBIT: The Measure of ATM Success

- TranSwitch *communicator* - Autumn 1994 edition (COM94AUT.PDF).
  Includes technical articles - Broadband Future in Europe
  New Chip Provides Voice and Video Capabilities for ATM

TranSwitch Product Line Information

- List of TranSwitch Products (**PRODLIST.PDF**).
  TranSwitch currently markets VLSI devices, product families, modules, evaluation boards and development support systems. Product names are listed alphabetically under these five headings. VLSI devices, product families and modules are marked as members of the Synchronous (S), Asynchronous (A) or Data Networking and Access (D) groups in this Index. Brief descriptions of products included in these groups are provided in the following three documents.

- Synchronous Product Line (**PL_SYNC.PDF**).
  A complete family of Synchronous Optical Network (SONET) / Synchronous Digital Hierarchy (SDH) products that implement synchronous transmission systems at 51.84 Mbit/s (STS-1), 155.52 Mbit/s (STS-3/STM-1) and 622.08 Mbit/s (STS-12/STM-4).

- Asynchronous Product Line (**PL_ASYNC.PDF**).
  A complete family of Plesiochronous Digital Hierarchy (PDH) / Asynchronous products that operate at all transmission speeds including DS1, E1, JT2, E2, DS3 and E3. These transmission speeds can also be conveniently mapped into higher speed SONET/SDH links using a full range of TranSwitch mapping products.

- Data Networking and Access (ATM) Product Line (**PL_DNA.PDF**).
  A complete family of Data Networking and Access (including Asynchronous Transfer Mode - ATM) products that provide a base for implementing a wide variety of public and private switching and multiplexing products.

Data Sheets (or the more concise Product Information Sheets) that are available on this CD-ROM disk for TranSwitch products in each of these three Product Line categories, and other associated documents (Application Notes, Technical Bulletins, etc.) are listed under separate headings on the pages that follow. Information on evaluation boards and development support systems is then shown under corresponding headings.

TranSwitch Product Documentation

In addition to its products intended for use in new designs, TranSwitch continues to provide documentation to support products in its Provisional Product, Manufacturing Discontinued and Not Available classifications:

Provisional Product (PP): Products which are not recommended for use in new designs, often due to introduction of an improved successor product, but are still being manufactured.

Manufacturing Discontinued (MD): Products which are not recommended for new designs and are no longer being manufactured, but may be available from stock.

Not Available (NA): Products which are no longer being manufactured, and for which there may be little or no stock available to fulfill orders.

In the following lists of TranSwitch products, those which are not intended for use in new designs have their product names shown in bold italic font and their classification indicated as PP, MD or NA.

Synchronous (SONET/SDH) VLSI Devices --- Data Sheets

Product Name | Product Number | Product Description & Data Sheet Ed./Release Date/File Name

- ***ADMA-E1***   TXC-04002   2-Mbit/s to TU-12 Async Mapper-Desync Device
  (MD)   Ed. 3A Aug 95 (AE1_3AD_.PDF).

- ADMA-E1   TXC-04002B   2-Mbit/s to TU-12 Async Mapper-Desync Device
  Ed. 2A Mar 97 **(AE1_2ADB.PDF)**.

- ***ADMA-T1***   TXC-04001   1.544 Mbit/s to VT1.5/TU-11 Async Mapper-Desync
  (MD)   Ed. 6 Jun 93 **(AT1_6_D_.PDF)**.

- ADMA-T1   TXC-04001B   1.544 Mbit/s to VT1.5/TU-11 Async Mapper-Desync
  Ed. 1A Sep 95 (AT1_1ADB.PDF).

- ADMA-T1P   TXC-04011   1.544 Mbit/s to VT1.5/TU-11 Async Mapper-Desync
  Ed. 1 Sep 95 (AT1P1_D_.PDF).

- ***CDR***   TXC-02624   SONET/SDH Clock and Data Recovery Device
  (NA)   Ed. 1 Jun 95 (CDR_1_D_.PDF).

- ***L3M***   TXC-03452   SDH/SONET Level 3 Mapper Device
  (PP)   Ed. 2 Nov 93 (L3M_2_D_.PDF).

- L3M   TXC-03452B   SDH/SONET Level 3 Mapper Device
  Ed. 2A Mar 97 **(L3M_2ADB.PDF)**.

- L4M   TXC-03456   SDH/SONET Level 4 Mapper Device
  Ed. 1 Jun 95 (L4M_1_D_.PDF).

- QE1M   TXC-04252   Quad E1 Mapper Device
  Ed. 1 Aug 96 (QE1M1_D_.PDF).

- QT1M   TXC-04251   Quad T1 Mapper Device
  Ed. 2 Dec 95 (QT1M2_D_.PDF).

- ***SM3***   TXC-02201   SONET 3:1 STS-3/STS-1 Mux/Demux Device
  (MD)   Ed. 4 Apr 94 (SM3_4_D_.PDF).

- ***SOT-1***   TXC-03001   SONET STS-1 Overhead Terminator Device
  (PP)   Ed. 10 Sep 94 (SO1_10D_.PDF).

- SOT-1   TXC-03001B   SONET STS-1 Overhead Terminator Device
  Ed. 1 May 96 (SO1_1_DB.PDF).

- SOT-1E   TXC-03011   SONET STS-1 Overhead Terminator Device (with
  Extended features) Ed. 2 Feb 97 **(SO1E2_D_.PDF)**.

Synchronous (SONET/SDH) VLSI Devices --- Data Sheets (continued)

- *SOT-3*　　　TXC-03003　　STM-1/STS-3/STS-3c Overhead Terminator Device
  (PP)　　　　　　　　　　　Ed. 4 Mar 95 (SO3_4_D_.PDF).

- SOT-3　　　　TXC-03003B　STM-1/STS-3/STS-3c Overhead Terminator Device
  　　　　　　　　　　　　　Ed. 1 Sep 95 (SO3_1_DB.PDF).

- *STAF*　　　　TXC-02623A/B　SONET/SDH Transceiver and Framer Device
  (NA)　　　　　　　　　　　Ed. 1 Jun 95 (STAF1_D_.PDF).

- SYN155　　　TXC-02301B　155-Mbit/s Synchronizer Device (data output only)
  (-BICL version MD)　　　　Ed. 4 Sep 94 (155 _4_DB.PDF).

- SYN155C　　TXC-02302B　155-Mbit/s Synchronizer Device (data and clock outputs).
  　　　　　　　　　　　　　Ed. 4 Sep 94 (155C4_DB.PDF).

Synchronous (SONET/SDH) VLSI Devices --- Other Documents

- Customer Advisory - Functional "B" ADMA-E1 Devices have replaced Functional "A" ADMA-E1 Devices, CA-519 Ed. 1A Jun 96 **(AE1_1AC1.PDF)**.

- Technical Bulletin - Differences Between the "A" and "B" Versions of the ADMA-E1 Device, TB-518 Ed. 2A Mar 97 **(AE1_2AB1.PDF)**.

- Technical Bulletin - Differences Between the "A" and "B" Versions of the ADMA-T1 Device, TB-512 Ed. 2 Sep 95 (AT1_2_B2.PDF).

- Application Note - B3ZS/HDB3 L3M Codec Operation, Ed. 1 Jan 95 (L3M_1_A1.PDF).

  Application Note - Jitter Test Results for the L3M Device, Ed. 1 Mar 95 (was L3M_1_A2.PDF).
  　　　　　　This document was discontinued after release of Ed. 4 of the CD-ROM.
  　　　　　　Please contact TranSwitch Applications Engineering for information.

- Application Note - Jitter Test Results for the L4M Device, Ed. 1 Feb 95 (L4M_1_A1.PDF).

- Technical Bulletin - Differences Between the "A" and "B" Versions of the SOT-1 Device, TB-516 Ed. 1 Feb 96 (SO1_1_B1.PDF).

- Application Note - Designing SDH/SONET Add/Drop Muxes for Ring Applications with TranSwitch SYN155C / SOT-3 / ADMA-E1/T1, AN-518 Ed. 1 Nov 95 (SO3_1_A1.PDF).

Asynchronous (PDH) VLSI Devices --- Data Sheets

Product Name   Product Number   Product Description & Data Sheet Ed./Release Date/File Name

- ART   TXC-02020   Advanced DS3/STS-1 Receiver/Transmitter Device
Ed. 4 Mar 95 (ART_4_D_.PDF).

- ARTE   TXC-02021   Advanced DS3/STS-1 Receiver/Transmitter Device
Ed. 4 Mar 95 (ART_4_D_.PDF).

- *DJB*
(MD)   TXC-03351   Quad DS1 Dejitter Buffer Device
Ed. 5 Mar 92 (DJB_5_D_.PDF).

- *DS3F*
(MD)   TXC-03401   DS3 Framer Device
Ed. 8 Jul 95 (DS3F8_D_.PDF).

- DS3F   TXC-03401B   DS3 Framer Device, Ed. 3A Mar 97 (**DS3F3ADB.PDF**).

- E123MUX   TXC-03361   E1/E2/E3 Mux/Demux Device
Ed. 2 Mar 97 **(EMUX2_D_.PDF).**

- *E2/E3F*
(NA)   TXC-03701   8-, 34-Mbit/s Framer Device
Ed. 4 Jun 93 **(E23F4_D_.PDF)**.

- E2/E3F   TXC-03701B   8-, 34-Mbit/s Framer Device
Ed. 1 Aug 95 (E23F1_DB.PDF).

- HDLC   TXC-05101C   HDLC Controller Device
Ed. 1A Apr 95 (HDLC1ADC.PDF).

- *JT2F*
(MD)   TXC-03702   6-Mbit/s JT2 Framer Device
Ed. 4 Nov 94 **(JT2F4_D_.PDF)**.

- JT2F   TXC-03702B   6-Mbit/s JT2 Framer Device
Ed. 1 Sept 95 (JT2F1_DB.PDF).

- *M12*
(PP)   TXC-03375   DS2/DS1 Mux/Demux Device
Ed. 3 Dec 94 (M12_3_D_.PDF).

- *M13*
(MD)   TXC-03301   DS3/DS1 Mux/Demux Device
Ed. 6 Sept 92 (M13_6_D_.PDF).

- M13E   TXC-03303   DS3/DS1 Mux/Demux, Extended Features Device
Ed. 3 Jun 96 (M13E3_D_.PDF).

- MRT   TXC-02050   6-, 8-, 34-Mbit/s Line Interface Device
Ed. 3 Apr 94 (MRT_3_D_.PDF).

- QDS1F   TXC-03102   Quad DS1 Framer Device
Ed. 5 Jun 95 (QD1F5_D_.PDF).

Asynchronous (PDH) VLSI Devices --- Data Sheets (continued)

- QE1F   TXC-03104   Quad E1 Framer Device
  Ed. 1 May 96 (QE1F1_D_.PDF).

- XBERT   TXC-06125   Bit Error Rate Generator/Receiver Device
  Ed. 3 Aug 95 (XBER3_D_.PDF).

(Modules)

- *DS3LIM*   TXC-20049D   DS3 Line Interface Module, NRZ Clock/Data Output
  (MD)   Ed. 3 Apr 94 (**D3LI3_DD.PDF**).

- **DS3LIM-SN**   TXC-20153D   DS3/STS-1 Line Interface Module, NRZ Clock/Data Output
                    TXC-20153G   Ed. 1A Nov 96 (**D3SN1AD_.PDF**).

Asynchronous (PDH) VLSI Devices --- Other Documents

- Application Note - Guidelines for ART / ARTE Circuit Board Layout, AN-406 Ed. 1 Dec 95 (ART_1_A1.PDF).

- Technical Bulletin - Differences Between the "A" and "B" Versions of the DS3F Device, TB-511 Ed. 2A Mar 97 (**DS3F2AB1.PDF**).

- Customer Advisory - Functional "B" E2/E3F Devices Have Replaced Functional "A" E2/E3F Devices, CA-513 Ed. 1 Aug 95 (E23F1_C1.PDF).

- Technical Bulletin - Differences Between the "A" and "B" Versions of the E2/E3F Device, TB-513 Ed. 1 Nov 95 (E23F1_B1.PDF).

- Customer Advisory - Functional "C" HDLC Devices Have Replaced Functional "B" HDLC Devices, CA-514 Ed. 1 Aug 95 (HDLC1_C1.PDF).

- Application Note - Designing with the TranSwitch JT2F and CDB Devices for Terminating ATM Applications at JT2 Rates, AN-405 Ed. 2 Mar 95 (JT2F2_A1.PDF).

- Technical Bulletin - Differences Between the "A" and "B" Versions of the M13 Device, TB-519 Ed. 1 Jul 96 (M13_1_B1.PDF).

- Application Note - A Programmable Logic Array Design to Support SLC®96/TR-8 Applications with the TranSwitch QDS1F, AN-519 Ed. 1 Nov 95 (QD1F1_A1.PDF).

- Application Note - Designing a Printed Circuit Board for use with either QDS1F or QE1F TranSwitch VLSI Devices, AN-522 Ed. 1 Jul 96 (QD1F1_A2.PDF).

- Application Note - Implementation of Payload Remote Loopback for Channels of the QE1F Device, AN-524 Ed. 1 Oct 96 (**QE1F1_A1.PDF**).

- Application Note - Designing with TranSwitch XBERT Devices for Byte Interface at the OC-12 Rate, AN-516 Ed. 1 Aug 95 (XBER1_A1.PDF).

Data Networking and Access (ATM) VLSI Devices --- Data Sheets

Product Name	Product Number	Product Description & Data Sheet Ed./Release Date/File Name
■ ALI-25C	TXC-07125	ATM 25 Mbit/s Line Interface Controller Device Ed. 3A Aug 96 (**A25_3AD_.PDF**).
■ ALI-25T	TXC-07225	ATM 25 Mbit/s Line Interface Transceiver Device Ed. 3A Aug 96 (**A25_3AD_.PDF**).
■ CDB	TXC-05150	ATM Cell Delineation Block Device Ed. 4 Oct 94 (CDB_4_D_.PDF).
■ *COBRA* (PP)	TXC-05427B	Constant Bit Rate ATM Adaptation Layer 1 Device Ed. 2 Aug 96 (COBR2_DB.PDF).
■ CUBIT	TXC-05801	ATM *CellBus* Switch Device Ed. 4 Aug 96 (CUBI4_D_.PDF).
■ SALI-25C	TXC-07625	Six ATM Line Interface at 25 Mbit/s Controller Device Ed. 2 Feb 97 (**SALI2_D_.PDF**).
■ SARA II	TXC-05551	ATM Segmentation and Reassembly Controller Device Ed. 2A May 96 (SA2_2AD_.PDF).
■ *SARA-R* (MD)	TXC-05601 (Ver. A & B)	ATM/SMDS Reassembly Controller Device (Tech. Manual) Ed. 6 Apr 96 (SARA6_M_.PDF).
■ *SARA-S* (MD)	TXC-05501 (Ver. A & B)	ATM/SMDS Segmentation Controller Device (Tech. Manual) Ed. 6 Apr 96 (SARA6_M_.PDF).

Data Networking and Access (ATM) Product Families --- Product Information Sheets

SARA-2	TXC-05551	SARA-2 ATM Segmentation and Reassembly Product Family:

■ Overview of SARA-2 Product Family, Ed. 1 Feb 97 (**OVERVIEW.PDF**).

■ SARA-2 ATM Cell Processing IC, Ed. 1 Feb 97 (**ATMCP_IC.PDF**).

■ SARA-2 Software Platform and Capabilities, Ed. 1 Feb 97 (**PLATFORM.PDF**).

■ Overview of SARA-2 Available Bit Rate (ABR) Processing, Ed. 1 Feb 97 (**ABR_PROC.PDF**).

■ SARA-Warp^{TM} ATM AAL0/5 Segmentation and Reassembly Solution,
Ed. 1 Feb 97 (**SARAWARP.PDF**).

■ SARA-Pro^{TM} ATM AAL0/1/3/45 Segmentation and Reassembly Solution,
Ed. 1 Feb 97 (**SARAPRO.PDF**).

Data Networking and Access (ATM) Product Families --- Product Information Sheets (cont'd)

- SARA-Miniport™ Device Driver Software, Ed. 1 Feb 97 (**MINIPORT.PDF**).

- SARA-NIC™ PCI-Based Development Board, Ed. 1 Feb 97 (**SARANIC.PDF**).

Data Networking and Access (ATM) VLSI Devices --- Other Documents

- Application Note - UTOPIA Interface for the ALI-25 Chip Set, Ed. 1 May 95 (A25_1_A1.PDF).

- Application Note - Designing with the TranSwitch CDB Device for Non-Terminating ATM Applications, AN-402 Ed. 1A May 94 (CDB_1AA1.PDF).

- Application Note - Designing with the TranSwitch JT2F and CDB Devices for Terminating ATM Applications at JT2 Rates, AN-405 Ed. 2 Mar 95 (JT2F2_A1.PDF).

- Technical Manual - *CellBus* Technical Manual and CUBIT Applications, Ed. 3B Jun 96 (CELL3BM1.PDF). Includes timing considerations formerly described in App. Note AN-520.

- Application Note - An Adaptive Clock Recovery Scheme for Constant Bit Rate Applications using the TranSwitch *COBRA* Device (TXC-05427B), AN-523 Ed. 1 Aug 96 (COBR1_A1.PDF).

- Technical Bulletin - Differences Between the "A" and "B" Versions of the *COBRA* device, TB-517 Ed. 1 Apr 96 (COBR1_B1.PDF).

- Application Note - UTOPIA Interface for the SARA Chip Set, Ed. 1 Apr 95 (SARA1_A2.PDF).

- Customer Advisory - Functional "B" SARA Devices Will Replace Functional "A" SARA Devices, Ed. 1 Aug 95 (SARA1_C2.PDF).

- Customer Advisory - "B" SARA Devices Have Replaced "A" SARA Devices --- Last-Time Buy Notification for "A" SARA Devices, CA-518 Ed. 1 May 96 (SARA1_C3.PDF).

- Technical Bulletin - Differences Between the "A" (20 MHz) and "B" (33 MHz) Versions of the SARA Chipset, TB-515 Ed. 1 Dec 95 (SARA1_B1.PDF).

Evaluation Boards --- Product Information Sheets

- ALI-25C EB   TXC-21025   ATM Line Interface at 25 Mbit/s Evaluation Board
Ed. 1A May 96 (A25_1AI_.PDF).

- ATM EISA EB   TXC-21103   SARA-S and SARA-R ATM Tx, Rx Evaluation Board
Ed. 3A May 96 (ATM_3AI_.PDF).

- DS3F EB   TXC-21005   DS3 Framer Evaluation Board Set
Ed. 2A May 96 (DS3F2AI_.PDF).

- DS3LIM EB   TXC-21049   DS3 Line Interface Module Evaluation Board
Ed. 1A May 96 (D3LI1AI_.PDF).

- E2/E3F-MRT   TXC-21037   E2/E3 Framer and Line Interface Evaluation Board
  EB   Ed. 2A May 96 (23MR2AI_.PDF).

- HDLC EB   TXC-21043   HDLC Terminal Evaluation Board
Ed. 1A May 96 (HDLC1AI_.PDF).

- HDLC/HSSI   TXC-21095   HDLC Sourced High Speed Serial Interface (HSSI)
  EB   Evaluation Board
Ed. 1A May 96 (HDHS1AI_.PDF).

- HSSI DTE EB   TXC-21087   High Speed Serial Interface (HSSI) Data Terminal
Equipment (DTE) Emulator Board
Ed. 1A May 96 (DTE_1AI_.PDF).

- JT2F-MRT EB   TXC-21047   JT2 Framer and Line Interface Evaluation Board
Ed. 2A May 96 (JTMR2AI_.PDF).

  L3M EBS   TXC-21179   L3M Evaluation Board System
(Please contact TranSwitch for information on this product.)

- MRT EB   TXC-21055   6-, 8-, 34-Mbit/s Line Interface Evaluation Board
Ed. 1A May 96 (MRT_1AI_.PDF).

- SOT-3/SYN155/   TXC-21061   SOT-3/SYN155/ADMA-E1 Evaluation Board
  ADMA-E1 EB   Ed. 1A May 96 (SSAE1AI_.PDF).

- XBERT EB   TXC-21075   XBERT Bit Error Rate Generator/Receiver
Evaluation Board
Ed. 1A May 96 (XBER1AI_.PDF).

Development Support Systems --- Product Information Sheets

- SARA-NIC™   TXC-21252   SARA-NIC™ PCI-Based Development Board for SARA-2
  Ed. 1 Feb 97 **(SARANIC.PDF)**.

- SONGEN-51E/ TXC-21007   Signal Generator-Analyzer: STS-1 Electrical and
  155F   STM-1/STS-3/STS-3c Fiber Optic Interfaces
                            Ed. 1A May 96 (SONG1AI_.PDF).

- SONGEN-155E/ TXC-21107   Signal Generator-Analyzer: STM-1/STS-3/STS-3c
  155F   Electrical and STM-1/STS-3/STS-3c Fiber Optic
                            Interfaces
                            Ed. 1A May 96 (SONG1AI_.PDF).

- SONGEN-PB1   TXC-20047   SONGEN Processor Board: Pointer, Parity and Alarms
                            Processor
                            Ed. 2A May 96 (SOPB2AI_.PDF).

Other Documents

- Technical Bulletin TB92-002, VLSI Device Pins With No Internal Connection,
  Ed. 1 March 1992 (ALL_1_B_.PDF).